



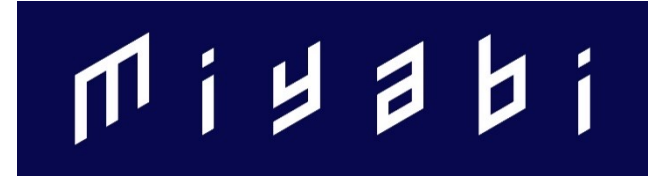
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JCAHPC



Introduction of Miyabi: AI for Science Based on Integration of “Simulation, Data, and Learning”

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JCAHPC

Information Technology Center
The University of Tokyo

JCAHPC

<http://jcahpc.jp/eng/index.html>



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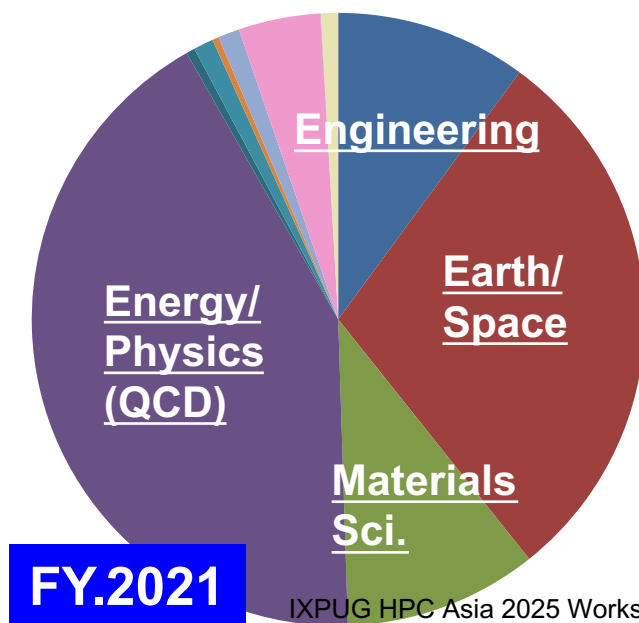
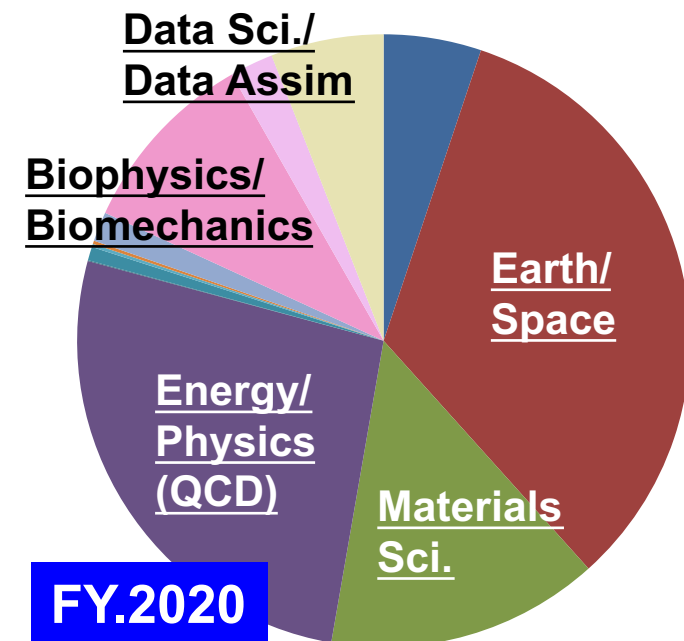
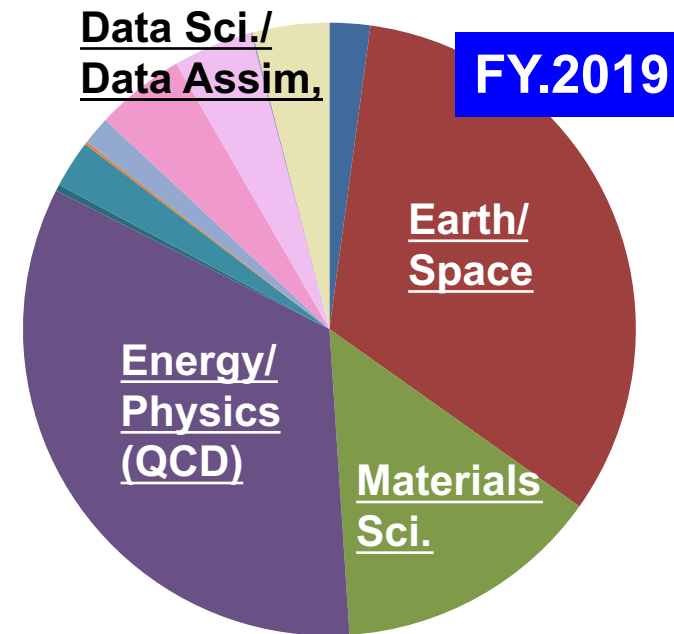
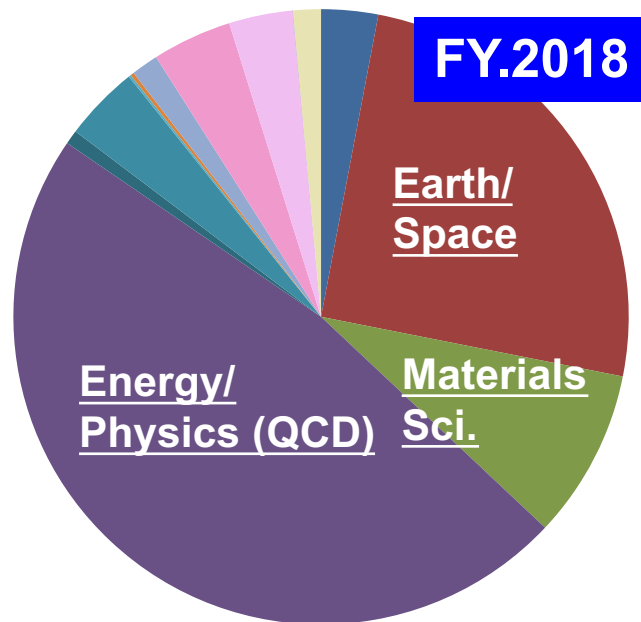
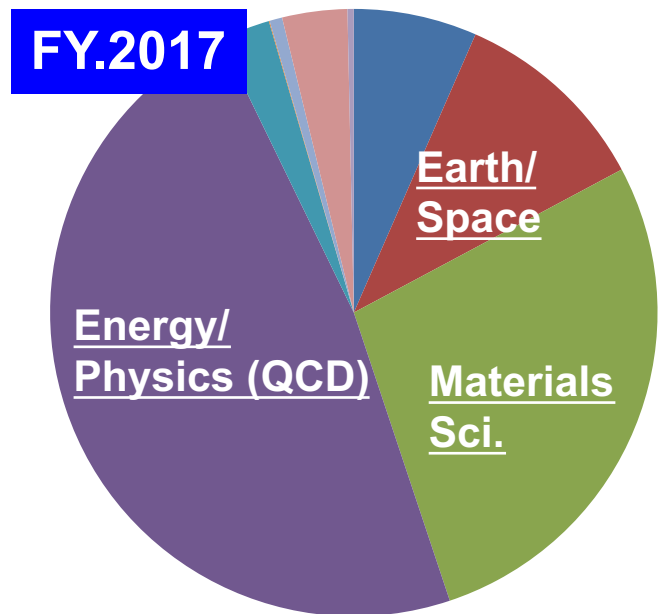


JCAHPC

- Joint Center for Advanced High Performance Computing, since 2013
 - University of Tsukuba & University of Tokyo
 - Budgets of 2 Centers are combined
 - Promotion on Computational Science, Design/Procurement/Operation of Large-scale Systems
- Oakforest-PACS (OFP), 1st System of JCAHPC
 - 8,208 Intel Xeon Phi, 25 PF, Fujitsu
 - Top 500 (#6 (Nov.2016), #1 in Japan)
 - Retired in the end of March 2022 (#39 (Nov.2021))
- National Flagship System (in fact) in FY.2019/2020
 - Between K and Fugaku



Research Area based on Machine Hours : OFP



- Engineering
- Earth & Space
- Materials Science
- Energy & Physics (QCD)
- Info. Sci.: System
- Info. Sci. : Algo.
- Info. Sci. : AI
- Education
- Industry
- Biophysics & Biomechanics
- Bioinformatics
- Social Science & Economics
- Data Science & Data Assimilation



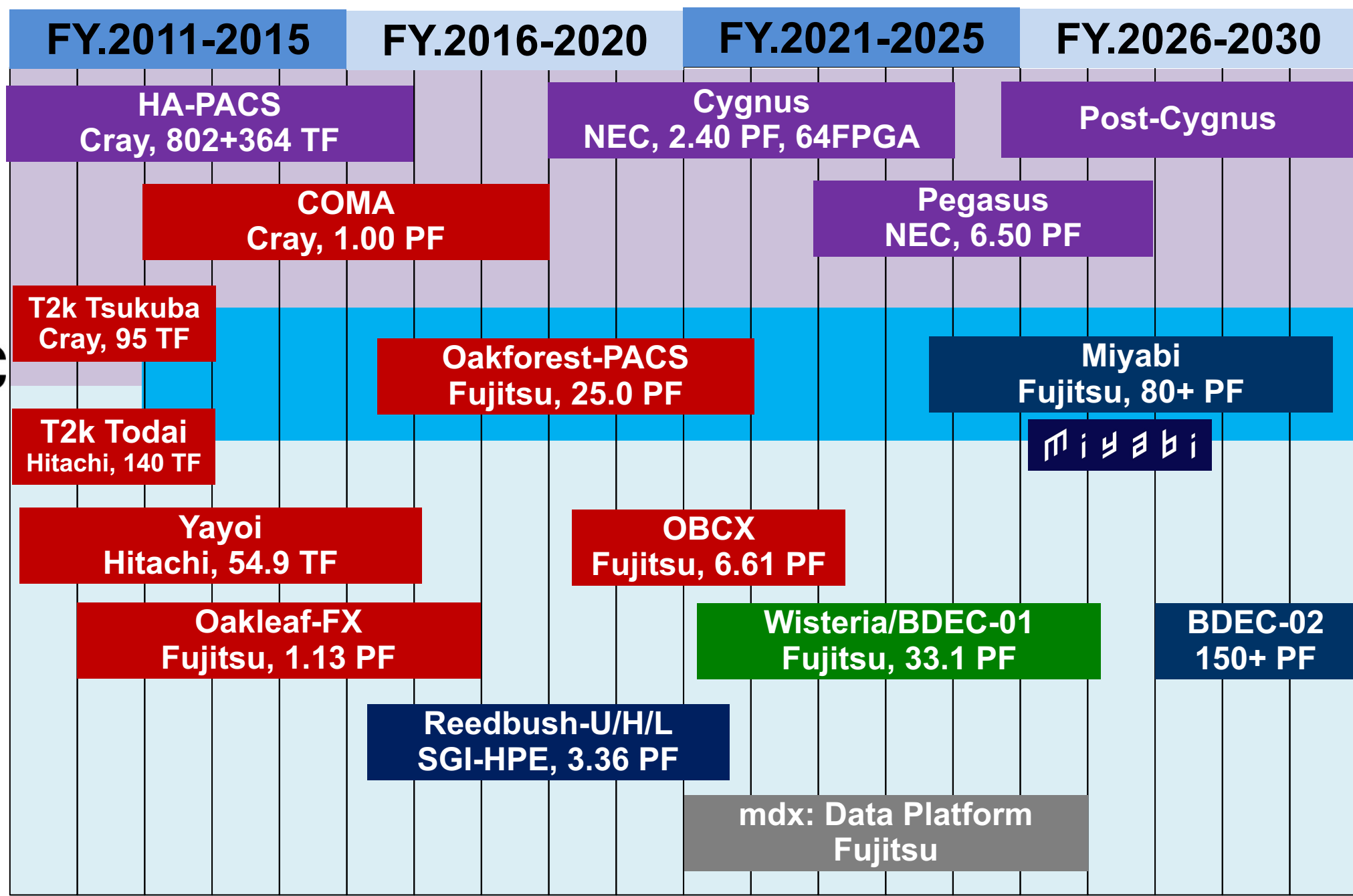
CPU

GPU

CPU/GPU

CPU/GPU

Cloud



About Miyabi

- System name was released in Apr. 2024.
- The name “**Miyabi**,” which means “**elegance and refinement**” in Japanese, carries more than just excellent theoretical performance; it encapsulates the intention to manifest its inherent abilities with less effort.
- Both centers will undertake the introduction and operation of the new “**Miyabi**” system in tight collaboration. By advancing highly sophisticated computational science through integrating “simulation, data, and learning,” we aim to contribute to the realization of a secure, safe, and reliable society.

Miyabi Overview



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Miyabi-G : 78.8 PFLOPS, 5.07 PB/s
(ACC node)

Miyabi-C : 1.3 PFLOPS, 608 TB/s
(CPU node)

Supermicro

CPU+GPU: NVIDIA GH200 Superchip

CPU: NVIDIA Grace

(72 core, 3.0 GHz, 117MB L3 Cache)

Mem: 120 GB (LPDDR5X, 512 GB/sec)

GPU: NVIDIA H100

(66.9 TFLOPS, NVLink-C2C 450 GB/sec/dir)

Mem: 96 GB (HBM3, 4.022 TB/sec)

× 1,120

Fujitsu PRIMERGY Server

CPU: Intel Xeon CPU Max 9480 x 2socket

(56 core, 1.9GHz, 112.5MB L3 Cache) x2

Mem: 128 GiB (HBM2E, 3.2 TB/sec)

× 190

InfiniBand NDR200
(200 Gbps)

InfiniBand NDR200
(200 Gbps)

InfiniBand NDR (400 Gbps), Full-bisection Fat-Tree

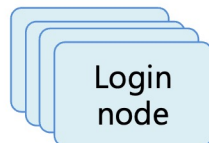
1.0 TB/s

Shared Filesystem
Lustre FS

11.3 PB
All Flash

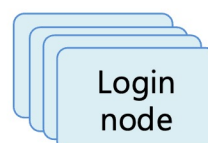
DDN ES400 NVX2 x10

Login node + Prepost



For CPU node
& Prepost

Intel Xeon 8480+ x2



For ACC node

NVIDIA Grace
CPU Superchip

External
Connecting
Router

Ethernet
RDMA



**Installation
& Operation
by Fujitsu**

Common Large-Scale
Filesystem (UTokyo)
Ipomoea-01
Lustre FS
25.9 PB

Miyabi Overview



Started from Jan. 14, 2025

Front racks are for management use



Miyabi-G Overview



There are also same-sized racks installed on the back

Miyabi-G: Accelerator node

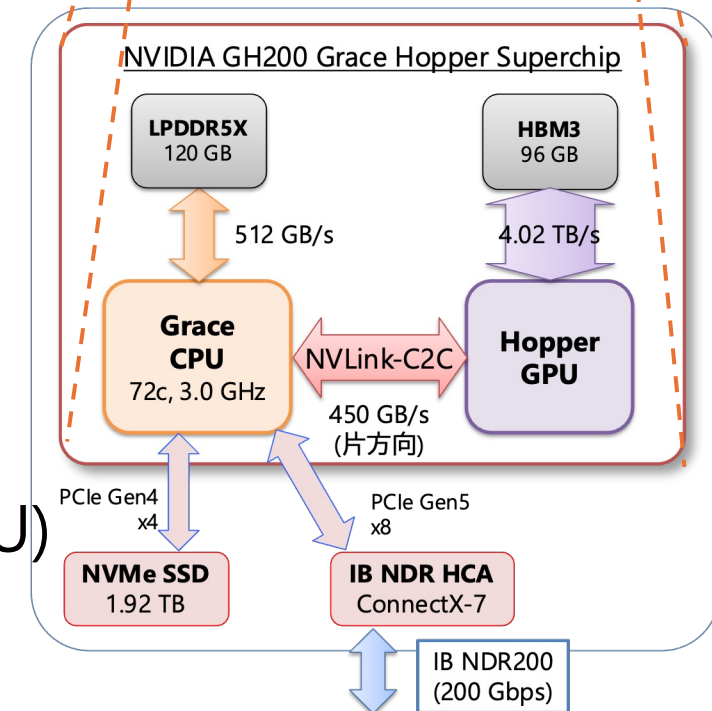


- **Compute node: CPU+GPU: NVIDIA GH200 Grace-Hopper Superchip**

- CPU: **Grace** 72 core, 3.0 GHz, 3.45 TFLOPS
 - Memory: 120 GByte, 512 GByte/sec (LPDDR5X)
- GPU: **Hopper** 66.9 TFLOPS (by Tensor core with double precision)
 - Memory: 96 GByte, 4.02 TByte/sec (HBM3)
- CPU-GPU: NVLink-C2C, 450 GB/sec(single dir.), Cache-coherent, porting of existing application is easier
- NVMe SSD: 1.9 TB, 8.0GB/sec, GPUDirect Storage
- InfiniBand NDR200

- **Miyabi-G Overall**

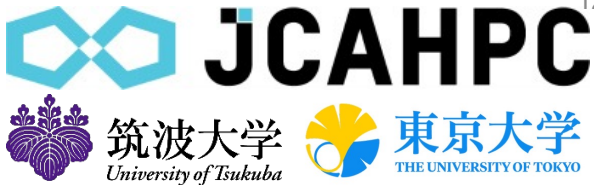
- **1,120 node** (18 rack、 64 node/rack)
- Theoretical peak performance: **78.8 PFLOPS** (CPU+GPU)
- Aggregate memory BW: 5.07 PByte/sec



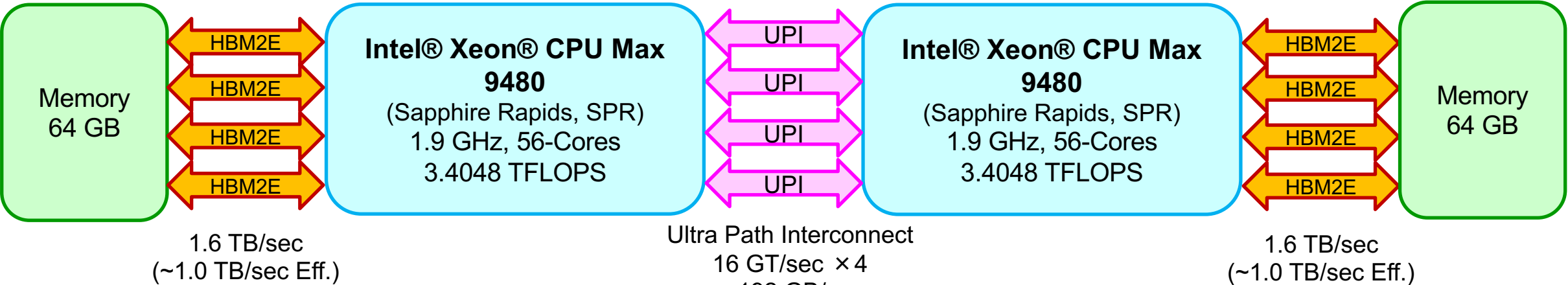
Miyabi-C overview



Miyabi-C: General Purpose CPU node



- **Compute node: CPU: Intel Xeon CPU Max 9480**
 - CPU: (Intel Xeon 9480: 56 core, 1.9 GHz) x 2 socket, 6.8 TFLOPS
 - Memory: 128 GB, 3.2 TB/sec (**HBM2E only**)
 - InfiniBand NDR200
- **Miyabi-C**
 - 190 node (3 rack, 64 node/rack)
 - Theoretical peak performance: 1.3 PFLOPS
 - Aggregate memory BW: 372 TByte/sec. (Stream Triad, Theoretical peak: 608 TByte/sec)

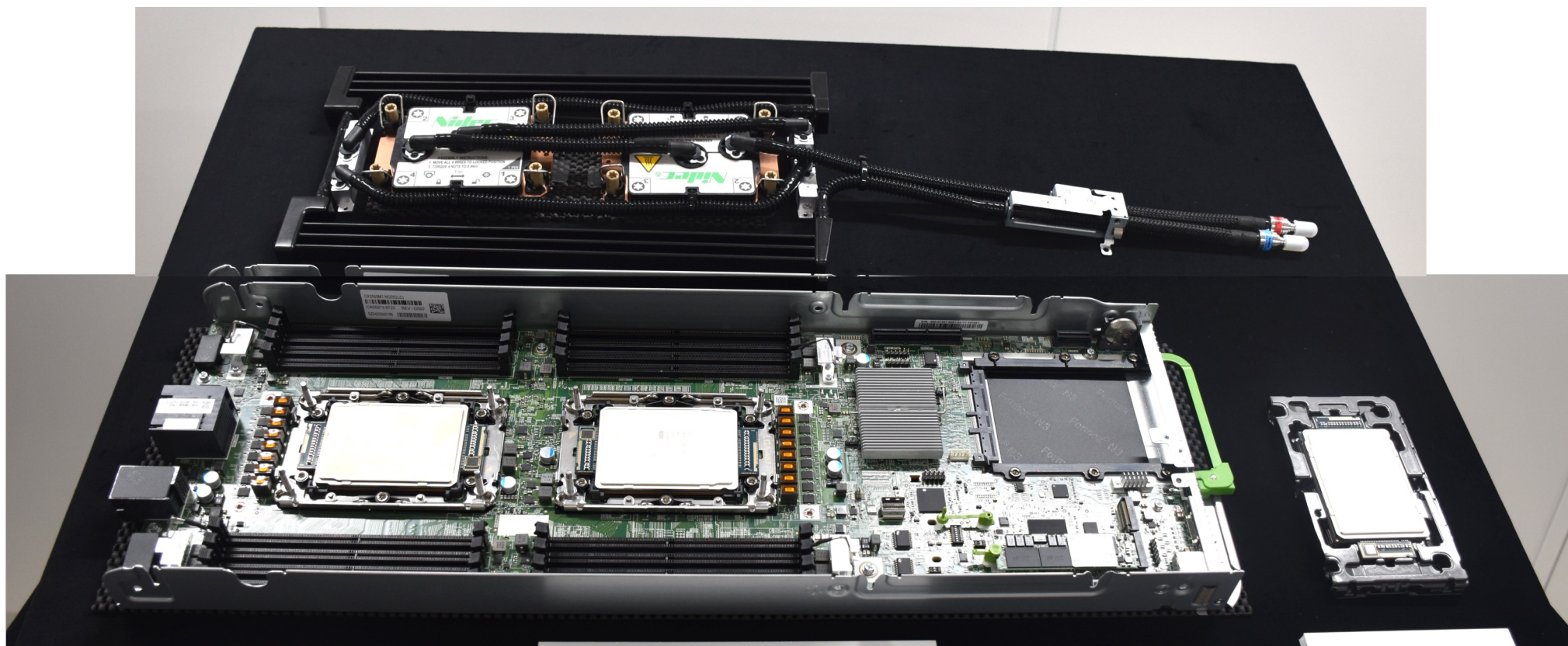


Miyabi-C Compute Node



- **Fujitsu PRIMERGY CX2550 M7**

- The memory is only HBM2E in the package, DIMMs are not implemented
- 4 nodes in 2U Chassis



What we will do with Miyabi

- 1. Computational Science !!!
- 2. AI for Science based on Integration of
(Simulation/Data/Learning)
- 3. QC-HPC Hybrid Computing





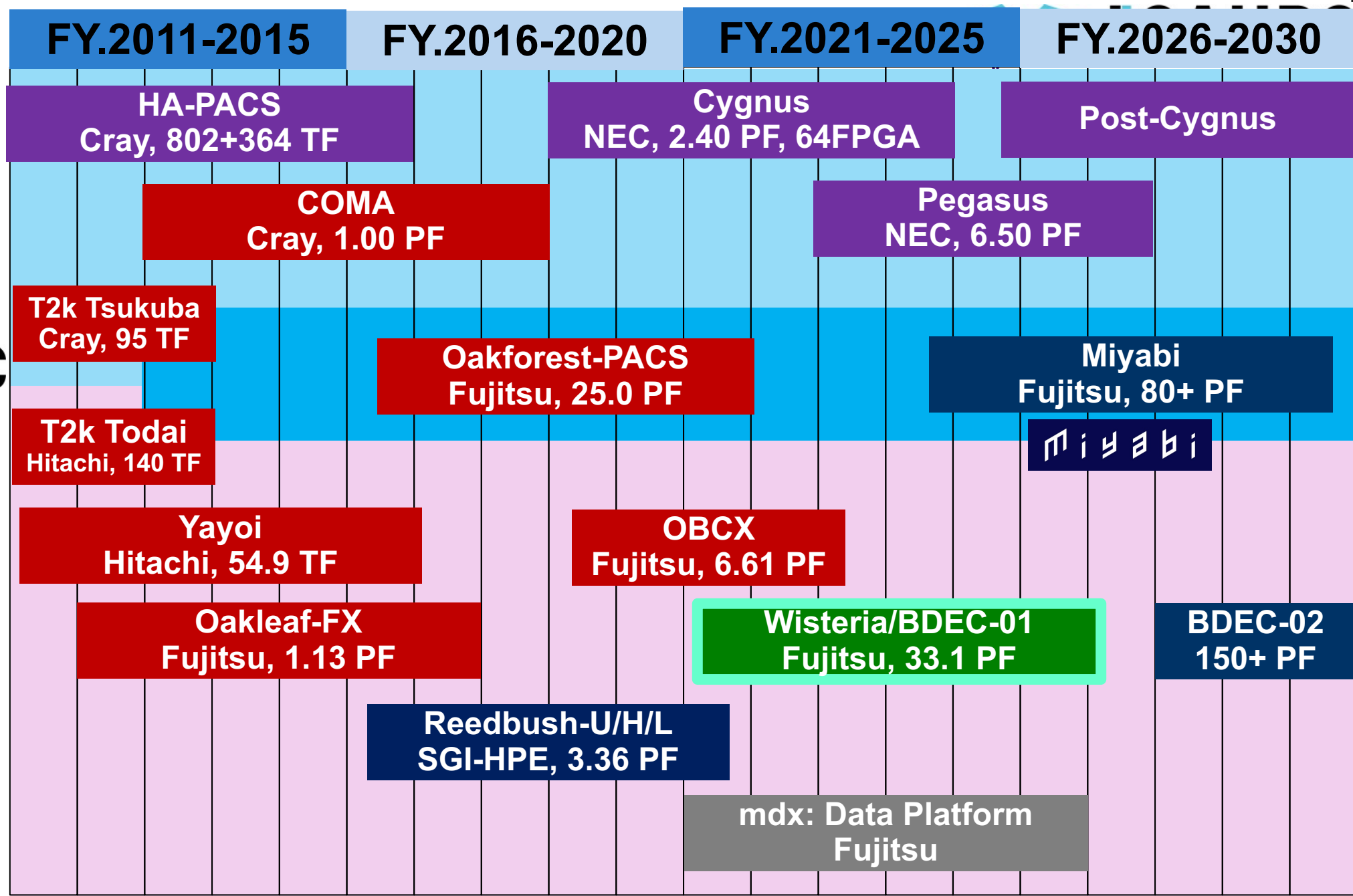
CPU

GPU

CPU/GPU

CPU/GPU

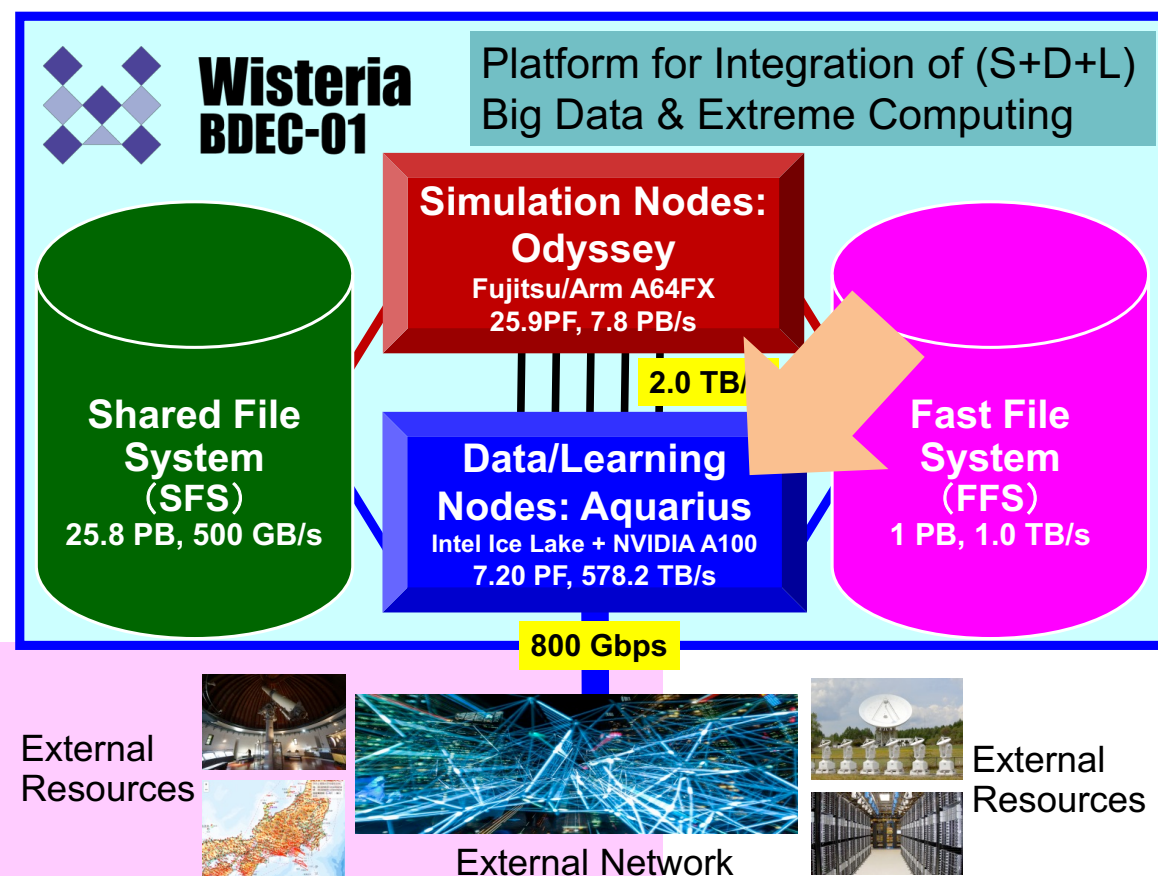
Cloud



Wisteria/BDEC-01

- Operation started on May 14, 2021
- 33.1 PF, 8.38 PB/sec by **Fujitsu**
 - ~4.5 MVA with Cooling, ~360m²
- **2 Types of Node Groups**
 - Hierarchical, Hybrid, Heterogeneous (h3)
 - **Simulation Node Group: Odyssey**
 - **Fujitsu PRIMEHPC FX1000 (A64FX), 25.9 PF**
 - 7,680 nodes (368,640 cores), Tofu-D
 - General Purpose CPU + HBM
 - Commercial Version of “Fugaku”
 - **Data/Learning Node Group: Aquarius**
 - **Data Analytics & AI/Machine Learning**
 - **Intel Xeon Ice Lake + NVIDIA A100, 7.2PF**
 - 45 nodes (90x Ice Lake, 360x A100), IB-HDR
 - **DL nodes are connected to external resources directly**
- File Systems: SFS (Shared/Large) + FFS (Fast/Small)

The 1st BDEC System (Big Data & Extreme Computing) HW Platform for Integration of (S+D+L)



**Wisteria
BDEC-01**

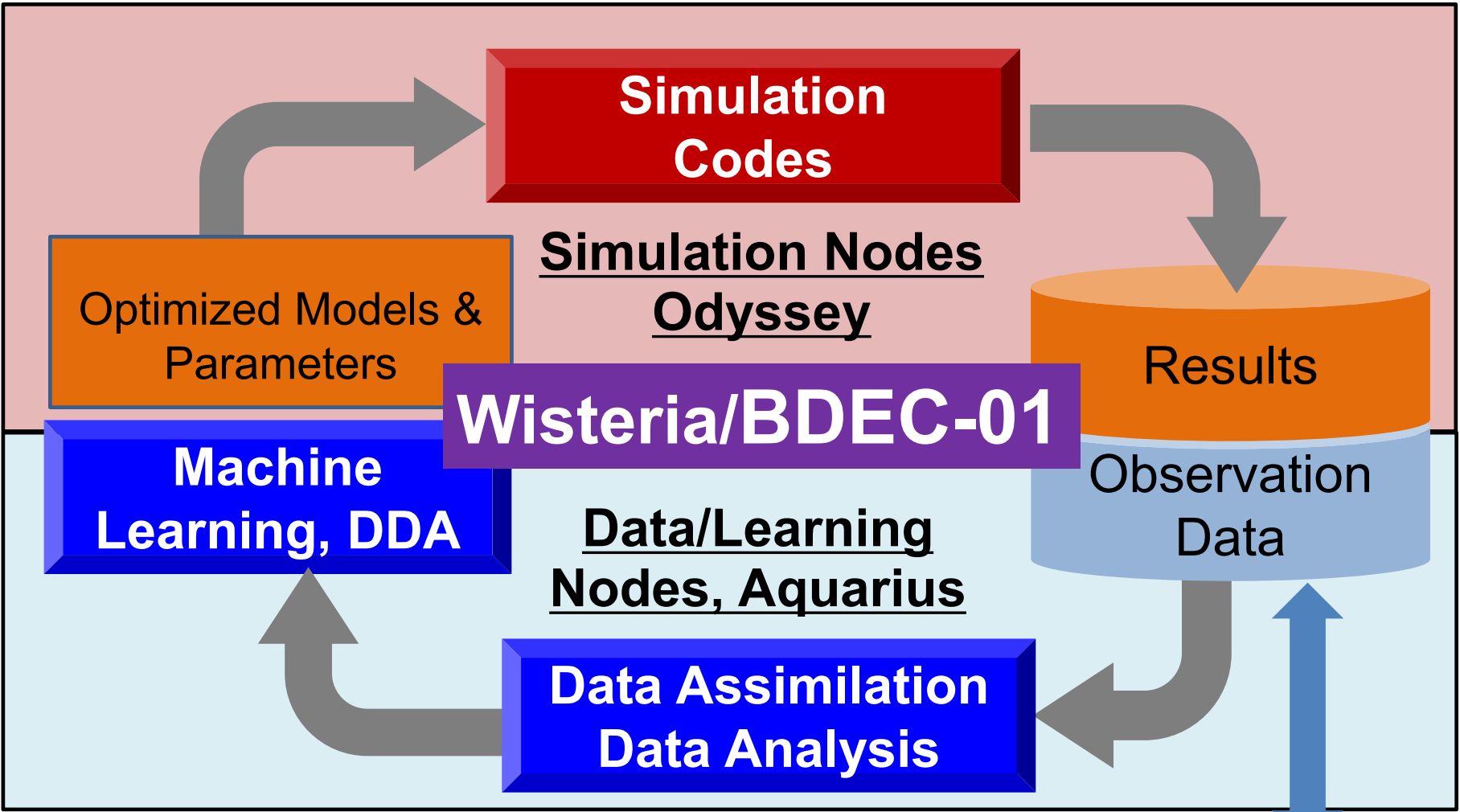


**Simulation Nodes
Odyssey**
25.9 PF, 7.8 PB/s

**Fast File
System
(FFS)**
1.0 PB,
1.0 TB/s

**Shared File
System
(SFS)**
25.8 PB,
0.50 TB/s

**Data/Learning Nodes
Aquarius**
7.20 PF, 578.2 TB/s



**Wisteria
BDEC-01**



Server, Storage, DB, Sensors, etc.

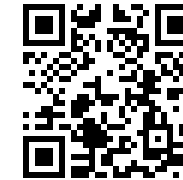
External Network

External Resources

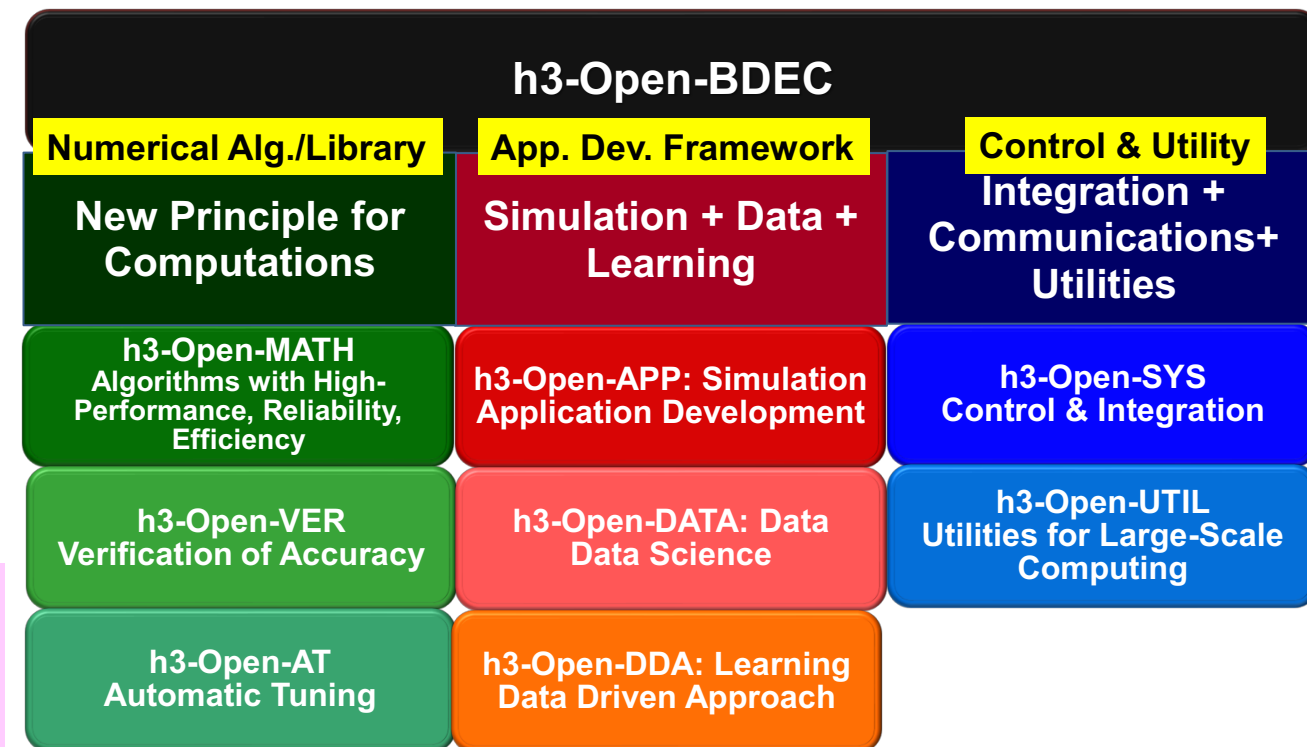
h3-Open-BDEC: Innovative Software Platform for Integration of (S+D+L)

FY.2019-2023 supported by JSPS

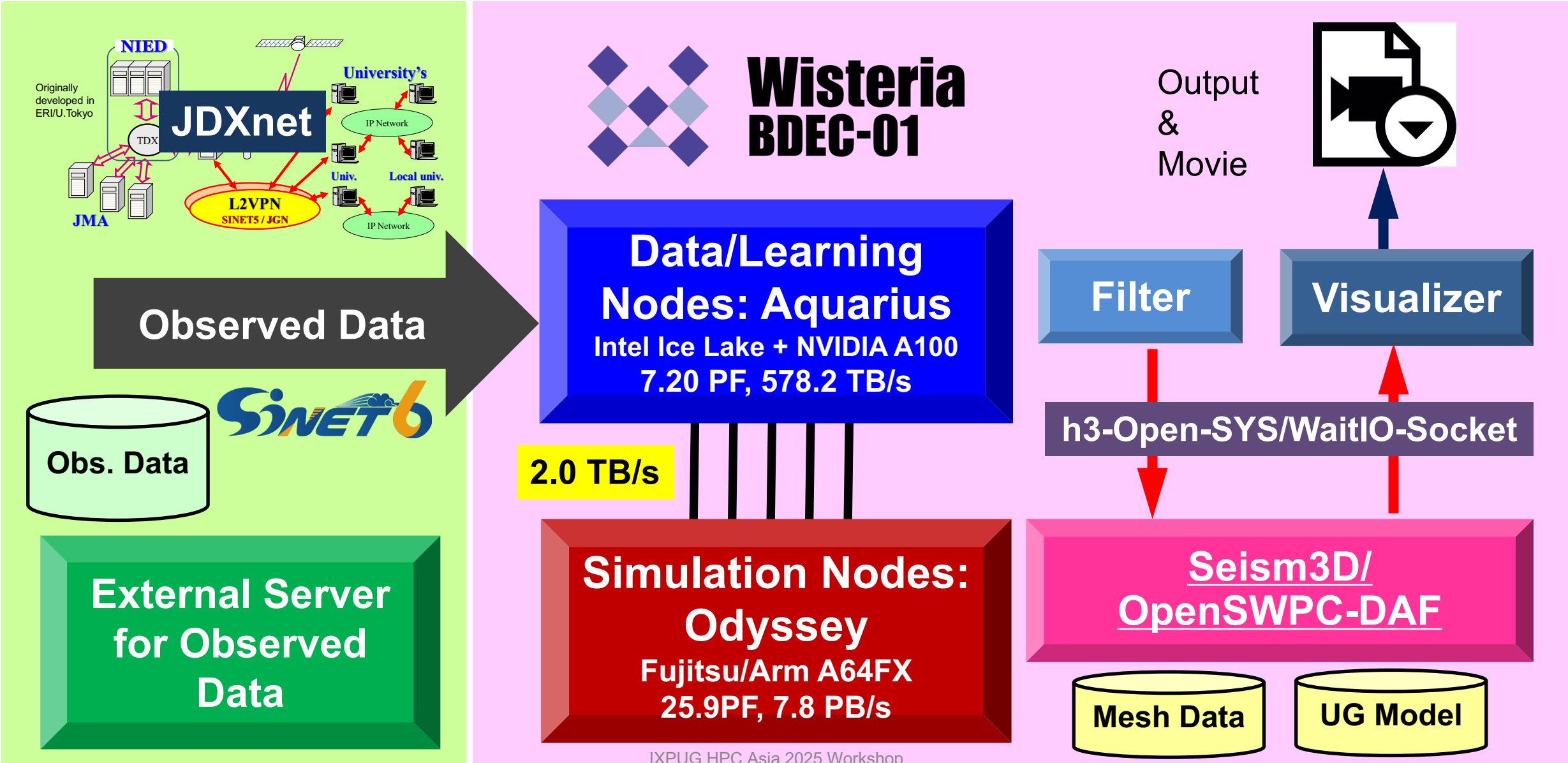
Leading PI: Kengo Nakajima



- “Three” Innovations
 - New Principles for Numerical Analysis by Adaptive Precision, Automatic Tuning & Accuracy Verification
 - Integration of (S+D+L) by Hierarchical Data Driven Approach (*hDDA*)
 - Software & Utilities for Heterogenous Environment, such as Wisteria/BDEC-01

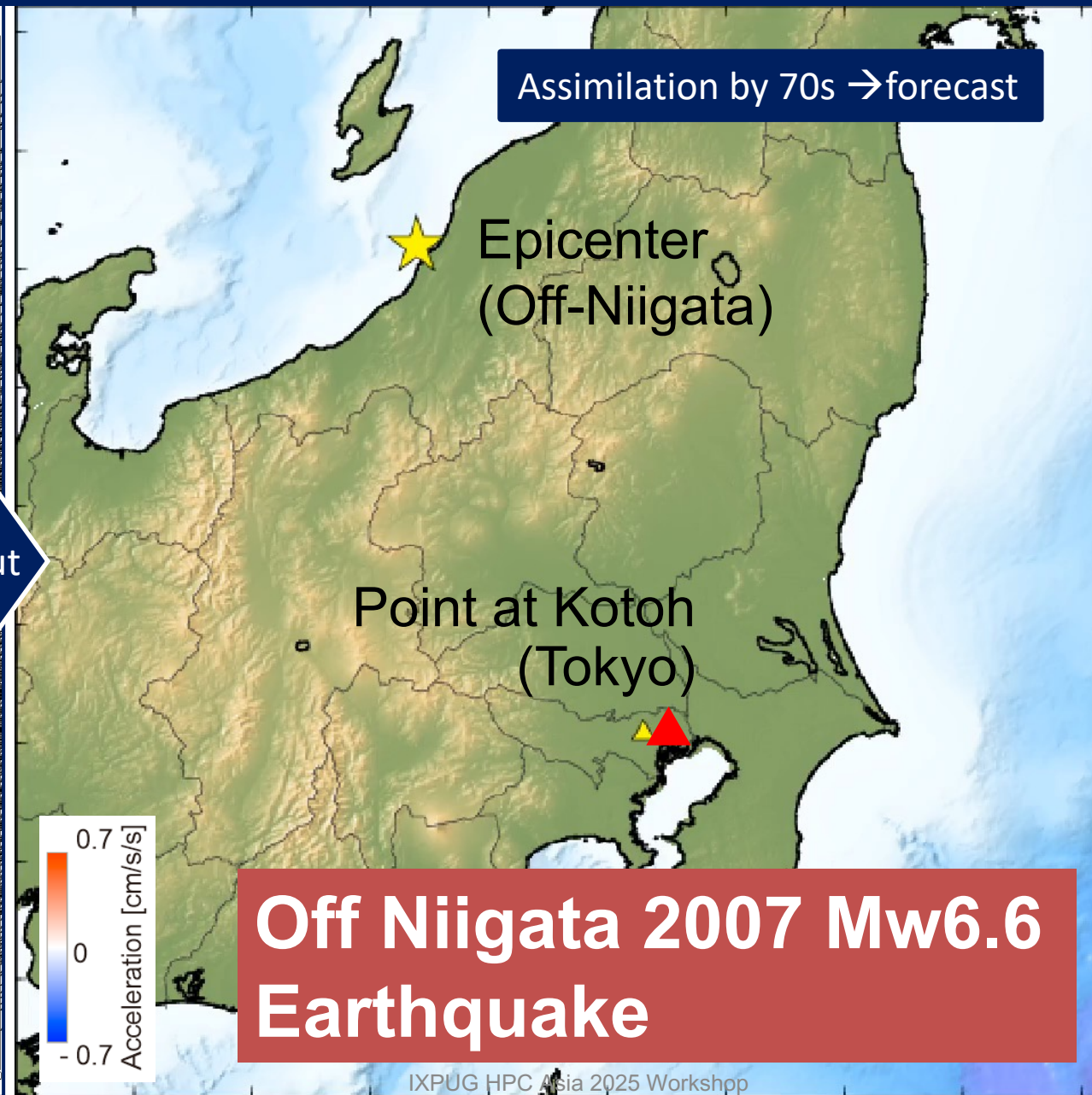
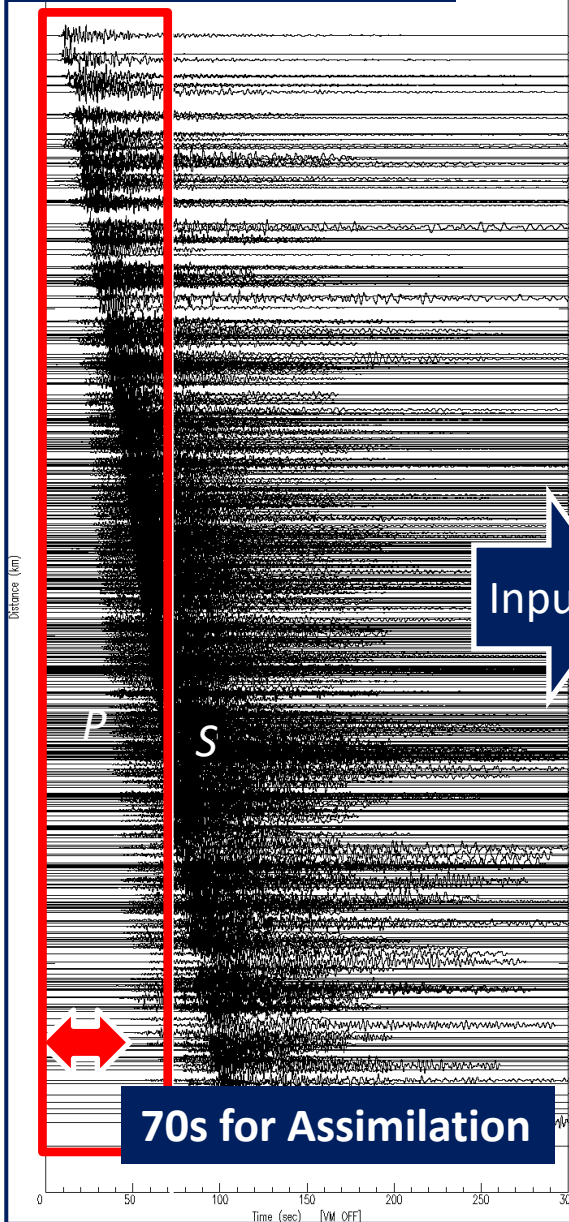


3D Earthquake Simulation with Real-Time Data Observation/Assimilation on Wisteria/BDEC-01

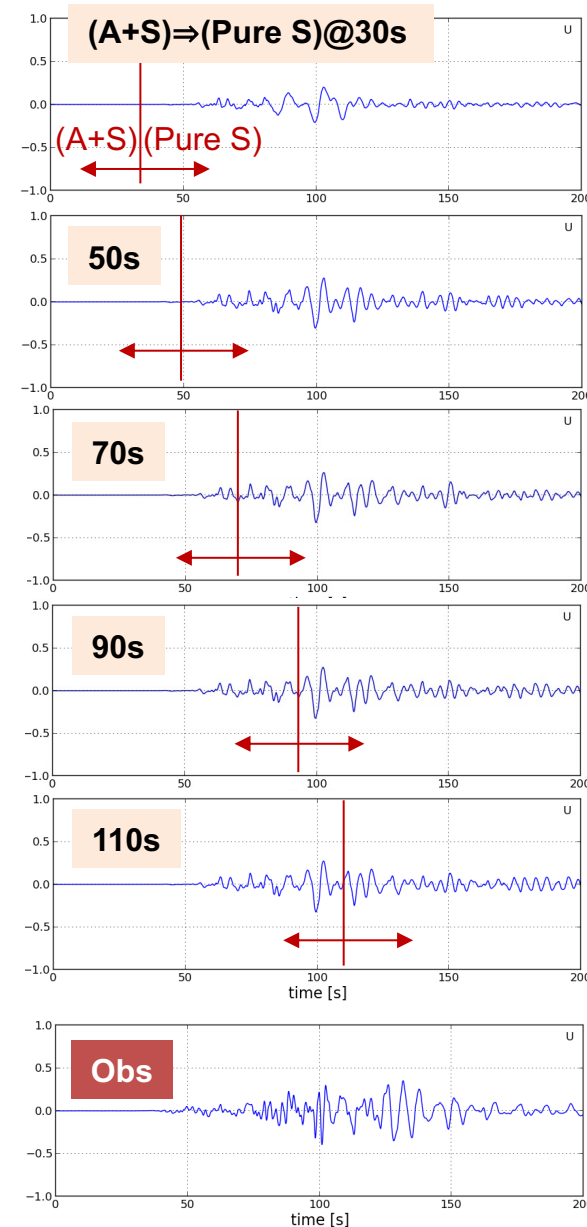


Data Assimilation + Pure Simulation/Forecast

482 K-NET, KiK-net Observation



Results at Kotoh ▲ (N.KOTH)
 N 35° 37.0'
 E 139° 46.9'



Communications by WaitIO-Socket

[Kasai et al. 2021]

Aquarius: SEND

```

program dmy_filter
<省略: 型宣言等>
call mpi_init (ierr)
call mpi_comm_size (MPI_COMM_WORLD, nprocs, ierr)
call mpi_comm_rank (MPI_COMM_WORLD, myrank, ierr)
call WAITIO_CREATE_UNIVERSE (WAITIO_COMM_UNIVERSE, ierr)

if (myrank==0) then
  open(100,file='./obsfile_list.txt', form='formatted', status='old', iostat=ierr)
  do i=1,300
    <省略: obsデータ読み込み処理>
    print *, "Send obs data ....."
    call WAITIO_MPI_ISEND (NTMAX1_o, 1, WAITIO_MPI_INTEGER, 2,1, WAITIO_COMM_UNIVERSE,req(1,1), ierr)
    call WAITIO_MPI_ISEND (DT_o, 1, WAITIO_MPI_FLOAT, 2,2, WAITIO_COMM_UNIVERSE,req(1,2), ierr)
    call WAITIO_MPI_ISEND (NST_o, 1, WAITIO_MPI_INTEGER, 2,3, WAITIO_COMM_UNIVERSE,req(1,3), ierr)
    call WAITIO_MPI_ISEND (AT_o, 1, WAITIO_MPI_FLOAT, 2,4, WAITIO_COMM_UNIVERSE,req(1,4), ierr)
    call WAITIO_MPI_ISEND (T0_o, 1, WAITIO_MPI_FLOAT, 2,5, WAITIO_COMM_UNIVERSE,req(1,5), ierr)
    call WAITIO_MPI_ISEND (ISO_X_o, NSMAX, WAITIO_MPI_INTEGER, 2,6, WAITIO_COMM_UNIVERSE,req(1,6), ierr)
    call WAITIO_MPI_ISEND (ISO_Y_o, NSMAX, WAITIO_MPI_INTEGER, 2,7, WAITIO_COMM_UNIVERSE,req(1,7), ierr)
    call WAITIO_MPI_ISEND (ISO_Z_o, NSMAX, WAITIO_MPI_INTEGER, 2,8, WAITIO_COMM_UNIVERSE,req(1,8), ierr)
    call WAITIO_MPI_ISEND (ISTX_o, NST, WAITIO_MPI_INTEGER, 2,9, WAITIO_COMM_UNIVERSE,req(1,9), ierr)
    call WAITIO_MPI_ISEND (ISTY_o, NST, WAITIO_MPI_INTEGER, 2,10, WAITIO_COMM_UNIVERSE,req(1,10), ierr)
    call WAITIO_MPI_ISEND (ISTZ_o, NST, WAITIO_MPI_INTEGER, 2,11, WAITIO_COMM_UNIVERSE,req(1,11), ierr)
    call WAITIO_MPI_ISEND (STC_o, 6*NST, WAITIO_MPI_CHAR, 2,12, WAITIO_COMM_UNIVERSE,req(1,12), ierr)
    call WAITIO_MPI_ISEND (VxAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 2,13, WAITIO_COMM_UNIVERSE,req(1,13), ierr)
    call WAITIO_MPI_ISEND (VyAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 2,14, WAITIO_COMM_UNIVERSE,req(1,14), ierr)
    call WAITIO_MPI_ISEND (VzAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 2,15, WAITIO_COMM_UNIVERSE,req(1,15), ierr)
    call WAITIO_MPI_WAITALL (15,req, status, ierr)
    call sleep(1)
  enddo
  close (100)
endif
call WAITIO_FINALIZE (ierr)
call mpi_finalize (ierr)
end

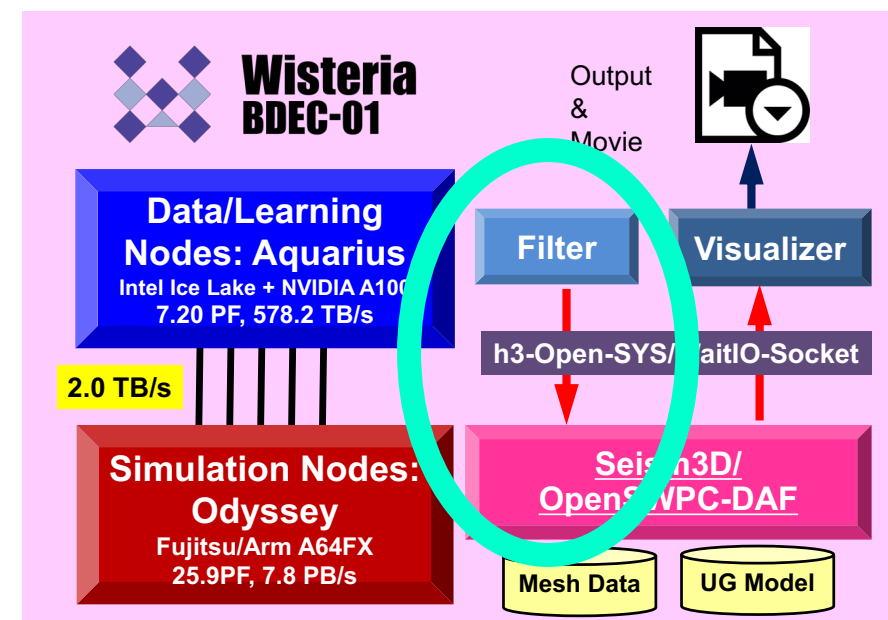
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Odyssey: RECV

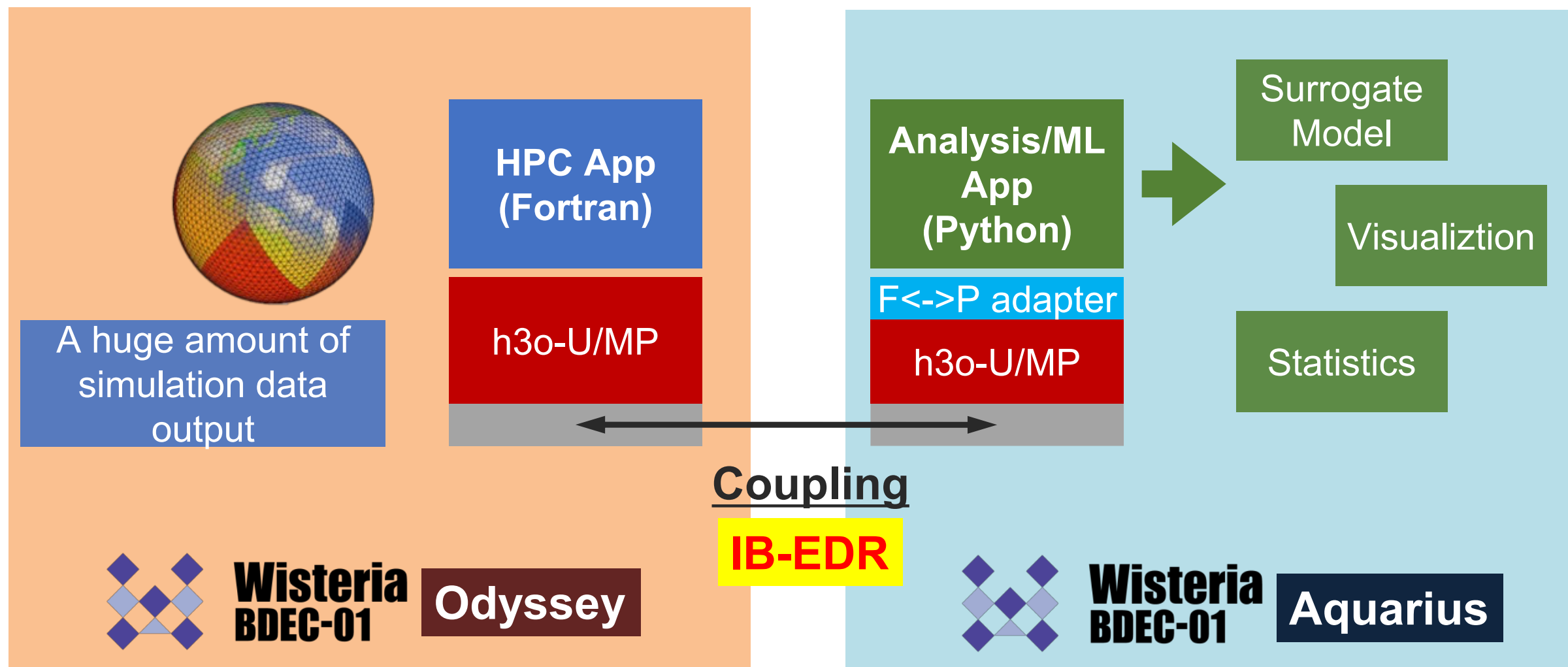
```

call WAITIO_MPI_Irecv (NTMAX1_o, 1, WAITIO_MPI_INTEGER, 0,1, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (DT_o, 1, WAITIO_MPI_FLOAT, 0,2, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (NST_o, 1, WAITIO_MPI_INTEGER, 0,3, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (AT_o, 1, WAITIO_MPI_FLOAT, 0,4, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (T0_o, 1, WAITIO_MPI_FLOAT, 0,5, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISO_X_o, NSMAX, WAITIO_MPI_INTEGER, 0,6, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISO_Y_o, NSMAX, WAITIO_MPI_INTEGER, 0,7, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISO_Z_o, NSMAX, WAITIO_MPI_INTEGER, 0,8, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISTX_o, NST, WAITIO_MPI_INTEGER, 0,9, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISTY_o, NST, WAITIO_MPI_INTEGER, 0,10, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (ISTZ_o, NST, WAITIO_MPI_INTEGER, 0,11, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (STC_o, 6*NST, WAITIO_MPI_CHAR, 0,12, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (VxAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 0,13, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (VyAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 0,14, WAITIO_COMM_UNIVERSE,...)
call WAITIO_MPI_Irecv (VzAll_obs,NST*NOBS_LEN,WAITIO_MPI_FLOAT, 0,15, WAITIO_COMM_UNIVERSE,...)

```



h3-Open-UTIL/MP (h3o-U/MP) Extended Multiphysics Coupler




[Sumimoto, Arakawa, Yashiro et al. 2022]



2. AI for Science (1/2)

based on Integration of (Simulation/Data/Learning)

- **Innovation in Scientific Research using Generative AI**
 - **Traditional “AI for Science”: Data-driven Science**
- ANL’s “Stormer (Short-Term Weather Forecasting)”
 - <https://www.anl.gov/cels/development-of-predictive-models-shortterm-forecasting>
- We support this type of activities of users, who want to utilize AI to CSE

Argonne  NATIONAL LABORATORY

RESEARCH WORK

COMPUTING, ENVIRONMENT AND LIFE SCIENCES

Development of Predictive Models: Short-Term Forecasting

Argonne is using state-of-the-art vision transformers to build one of the most accurate medium-range weather forecasting models that runs at a fraction of the cost compared to traditional Numerical Weather Prediction.

Argonne used observation-based reanalysis to train a custom, weather-specific vision transformer (Stormer) to predict the global atmosphere 14 days into the future. Using novel training techniques and creating a model-based ensemble system, Stormer produces more accuracy forecasts at unprecedented speed. Once trained, our model can produce a 14-day forecast composed of 32 ensemble members in under a minute, something not feasible using previous methods.

We compared Stormer to existing models found it performs competitively at short to medium-range forecasts and outperforms current models beyond 7 days. The accelerated speed of our model will allow for new possibilities in data assimilation, ensemble forecasting, and uncertainty quantification.

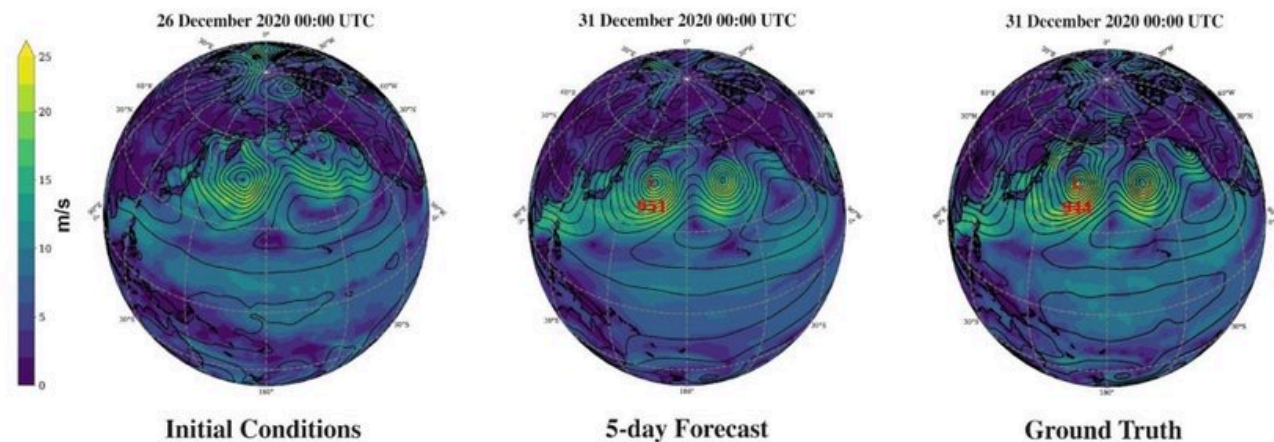


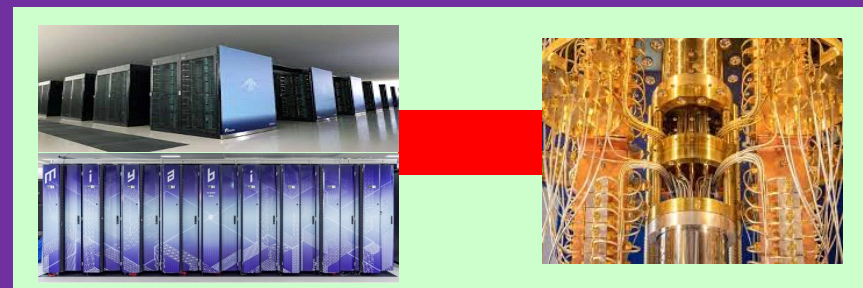
Figure 1: On December 31, 2020, an extratropical cyclone impacted Alaska setting a new North Pacific low-pressure record. Stormer forecasts both the location and strength of this extreme event with great accuracy 5 days in advance.

3. QC-HPC Hybrid Computing

- JHPC-quantum (FY.2023-FY.2028)
 - <https://jhpc-quantum.org/>
 - RIKEN, Softbank, U.Tokyo, Osaka U.
 - supported by Japanese Government (METI/NEDO)
 - Two Real Quantum Computers will be installed
 - IBM's Superconducting QC at RIKEN-Kobe (100+Qubit)
 - Quantinuum's Ion-Trap QC at RIKEN-Wako (20+Qubit)
 - Target Applications
 - Quantum Science, Quantum Machine Learning etc.
- Role of U.Tokyo
 - R&D on System SW for QC-HPC Hybrid Environment (QC as Accelerators)
 - Extension of h3-Open-BDEC
- **Fugaku & Miyabi will be connected to QCs in March 2026 (or before)**



CPU		GPU			Others	
A64FX Arm	X86	NVIDIA Intel AMD Arm	AMD	Intel	Sambanova Cerebras Graphcore etc.	Quantum
h3-Open-SYS/WaitIO						





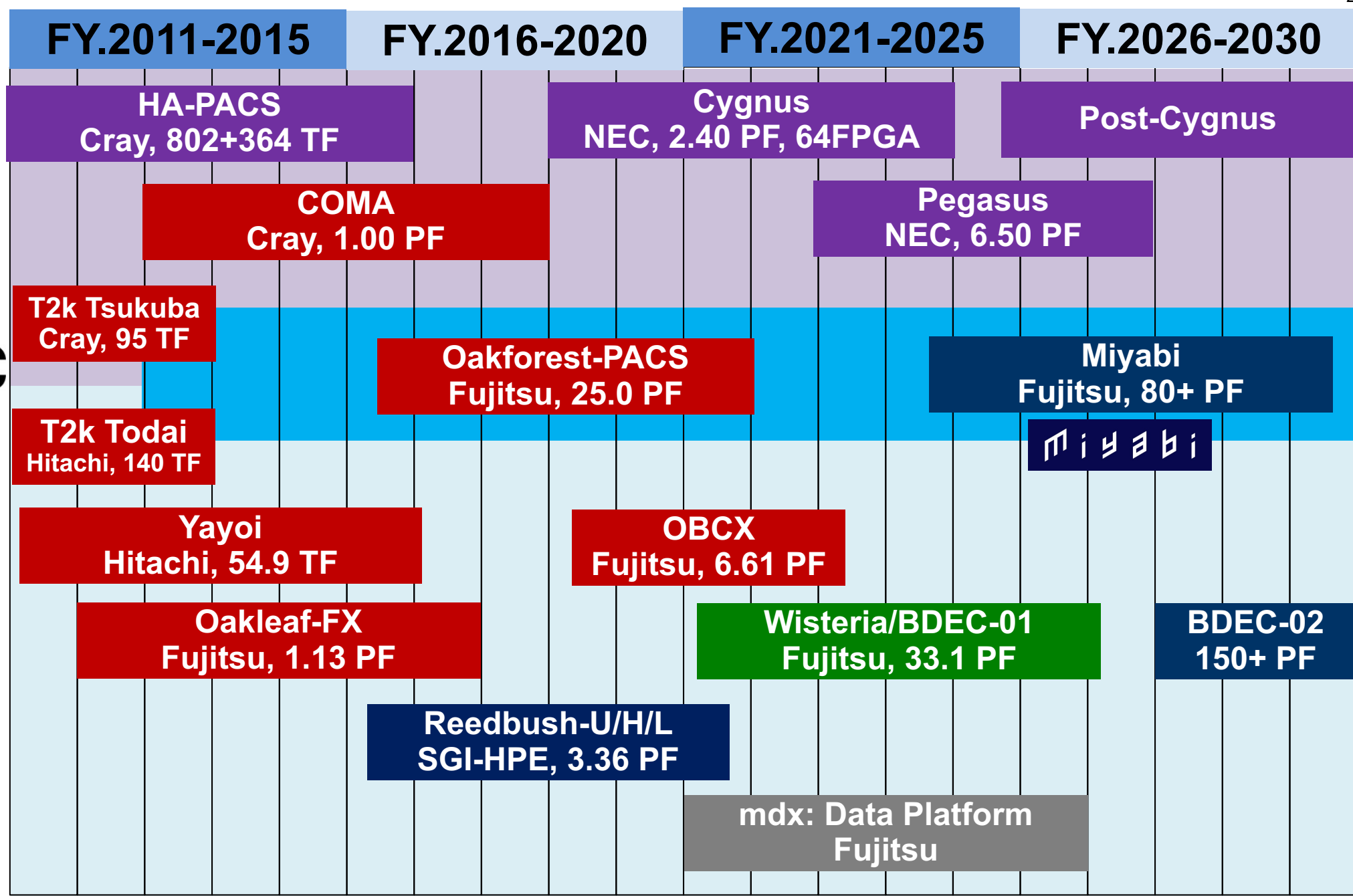
CPU

GPU

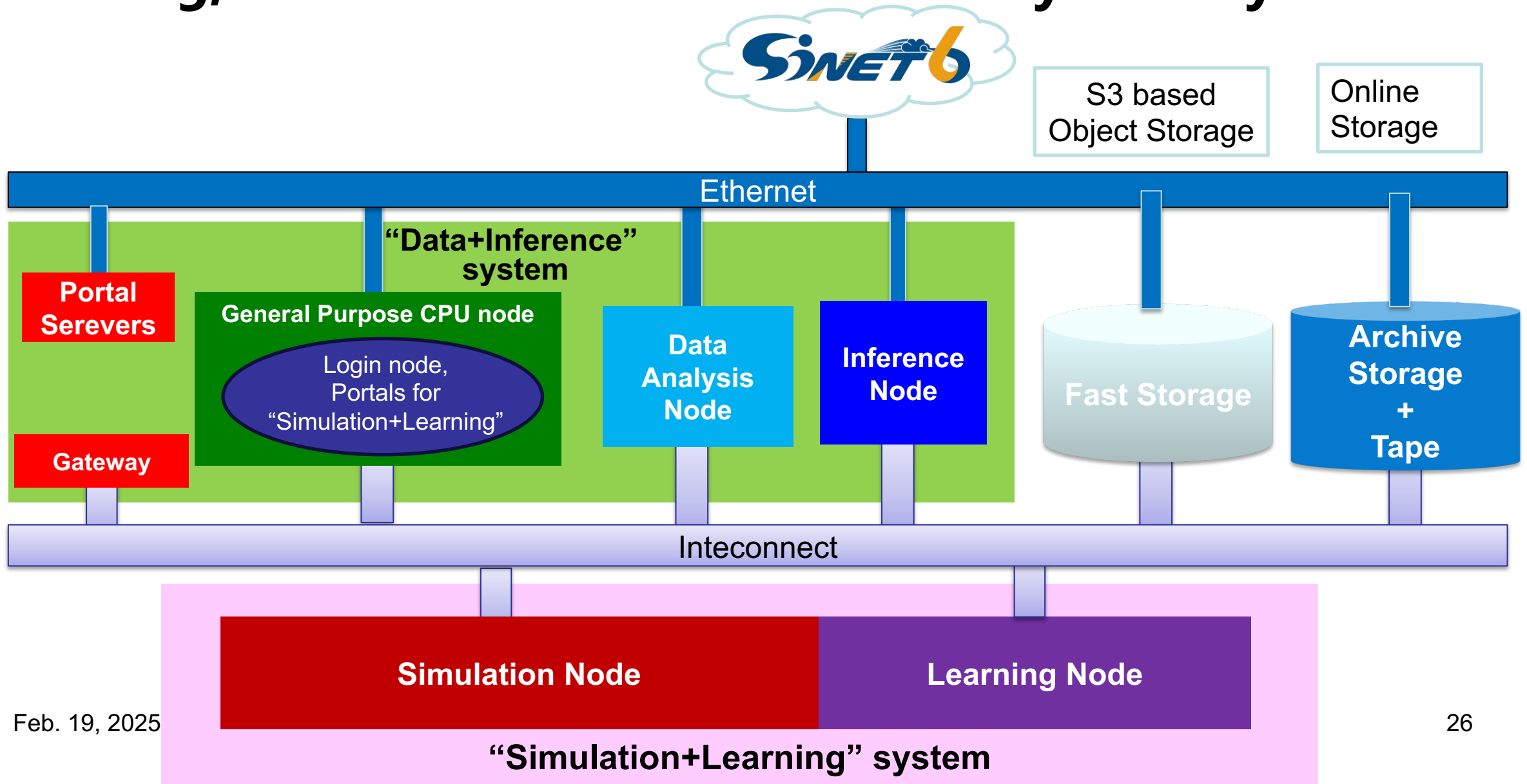
CPU/GPU

CPU/GPU

Cloud

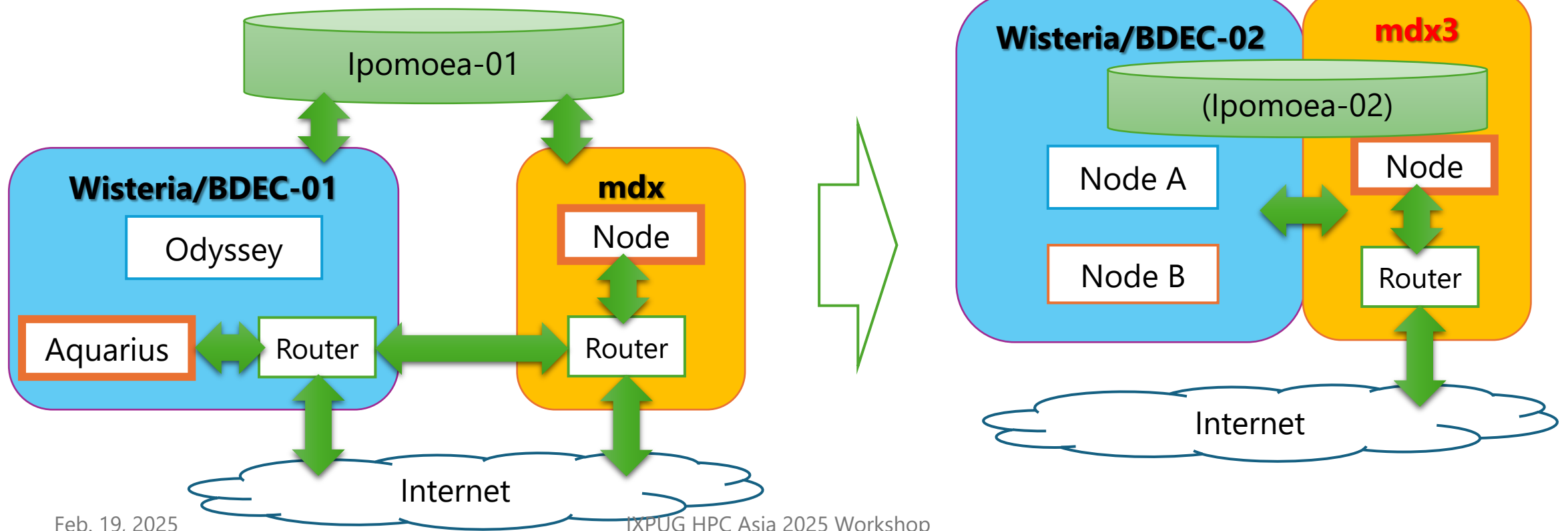


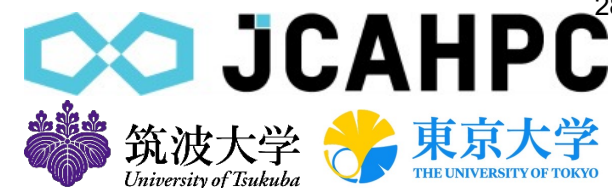
“Integrated Infrastructure System for Simulation, Data, Learning, and Inference” in the University of Tokyo



Integrated Infrastructure System for Computation, Data, Learning, and Inference

- Saving costs by sharing computer resources as much as possible and by avoiding redundant configurations.
 - Currently, Wisteria/BDEC-01 and mdx are separate systems in the same room





Summary

- JCAHPC, Oakforest-PACS
- Integration of (Simulation/Data/Learning)
- **What we will do with Miyabi**
 - **Computational Science, Computational Science, Computational Science !!!**
 - **AI for Science**
 - **QC-HPC Hybrid Computing**

