IXPUG Annual Conference 2023

Kairos

Innovation in Advancing HPC and AI Application Performance Analysis

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- Performance varies by use, configuration and other factors. Learn more on the <u>Performance</u> <u>Index site</u>.
- Performance results are based on testing as of dates shown in configurations and may not reflect all publicly available updates. See backup for configuration details. No product or component can be absolutely secure.
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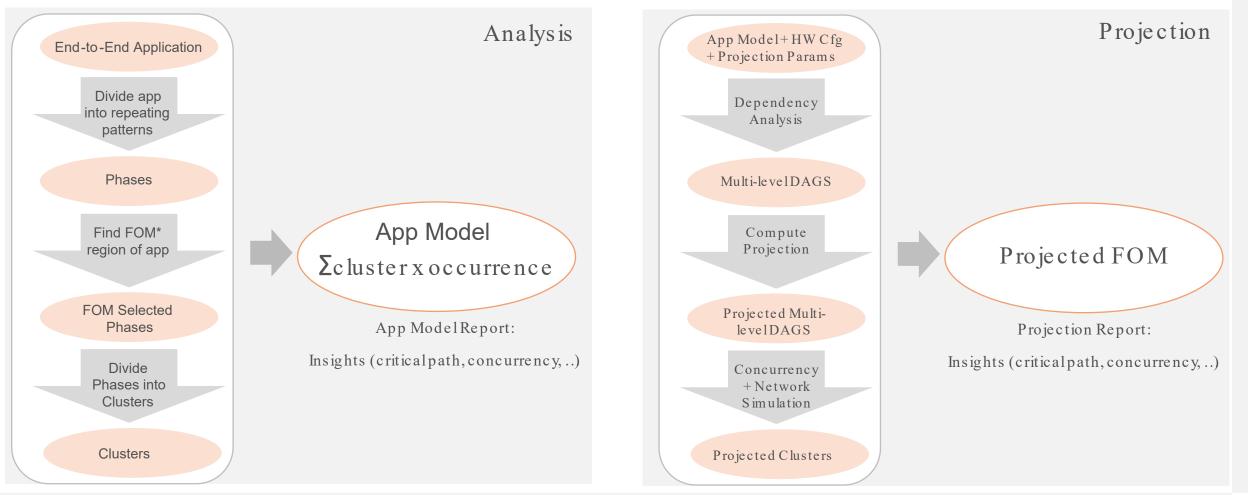
Executive Summary

Kairos is a unique capability not available inside or outside of Intel

- Methodology to analyze and project performance at system level
 - Top-down view of application, enables zoom -in to region of interest
 - Predicts impact of component performance on end -to-end application performance
 - Provides insights: critical path analysis, bottleneck analysis, concurrency behavior
- Key features
 - Quick overall understanding of application behavior
 - Visualize interactions between concurrent execution regions
 - Pathfinding/what -if capability

Kairos Flow

Methodology to analyze and project performance at system level



*FOM = Figure of Merit IXPUG Annual Conference 2023

Kairos Components

Tracing/Profiling

- MPI Shim
- CPU Shim
- GPU Shims:
 - OpenMP, SYCL, CUDA*, OpenCL
- AI Shims: OneCCL , NCCL*, Pytorch / Tensorflow profilers

Simulation

- Concurrency Model
- Network/communication simulation

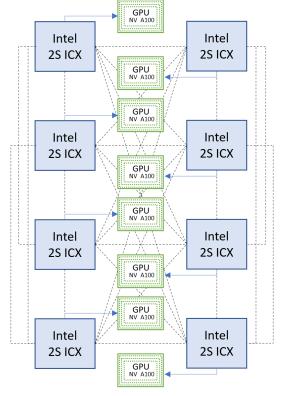
Reports/Profiles

- System trace (CPU, GPU, MPI, Mem, ...)
- Visualize/Summarize/Compare data/Insights
- Break up app/zoom into areas of interest

Kairos Methodology With an Example

- Picked an example to show case methodology
- Example:
 - LAMMPS on 8 Nodes
 - 2 socket ICX and a single A100 GPU per node
 - 32 ranks per node, 2 OpenMP threads per rank; total of 256 ranks, 512 threads
 - MPS disabled on A100 GPUs
 - Figure of Merit (FOM) from run log:

Loop time of 35.7078 on 512 procs



Cluster of 8 ICX Nodes with 1 A100 GPU each Fat-Tree Interconnect (indicative only)

Run is not meant to be performant run, solely meant to showcase Kairos methodology Thank you to Mike Brown for his guidance on LAMMPS runs

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Application Model: Kairos Phases

Capture run behavior with Kairos shims

- Divide app into repeating patterns
- For MPI Apps \rightarrow MPIPhases
- For AIApp → AIDistributed Phases

Phases captured across ranks using run behavior heuristics + collective APIs

Methodologyscales

• Phase numbers/behavior similar as user changes num ranks, num threads, SW configs, HW configs

Phases repeat, e.g. Phase 1292 occurred 624 times

Breaking app into phases using collectives and runtime behavior



Phase 1292 + 1293 + 1298 take significant amount of time

Application Model: Kairos Full App Trace

Find phases that make up Figure of Merit (FOM)

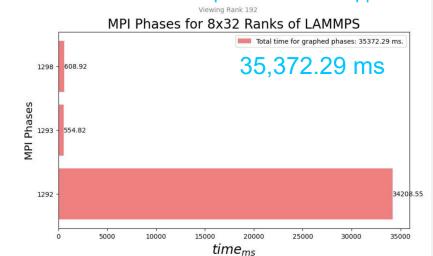
From Run Log: Loop time of 35.7078 on 512 procs

			10 s	20 s	30 s			
	▼ Process Rank '0'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000			1346		
Dracase Dank 11	 Process Rank '1' 			Satur Dagion		EOM		
	MPI Collectives	MPI_INIT		Setup Region		FOM		
	MPI Phase		1000	1 0		1346		
	▼ Process Rank '2'							
	MPI Collectives	MPI_INIT				35.7 ms		
	MPI Phase		1000	1003		33.7 III3		
	▼ Process Rank '3'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000	1003		1346		
	▼ Process Rank '4'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000	1003		1346		
	Process Rank '5'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000	1003		1346		
	Process Rank '6'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000	1003		1346		
	Process Rank '7'							
	MPI Collectives	MPI_INIT				THEY NEED THEY NEED THEM THEN THE ATTACK AND ALL THEY AND THE ATTACK AND ALL THE ATTACK AND ALL THE ATTACK AND A		
	MPI Phase		1000	1003		1346		
	▼ Process Rank '8'							
	MPI Collectives	MPLINIT				The second		
	MPI Phase		1000	1003		1346		
	Process Rank '9'							
	MPI Collectives	MPI_INIT				THEY BUT LINE AND THE THE THE STAR AND THE ADDA THE THEY AND THE		
	MPI Phase		1000	1003		1346		
	▼ Process Rank '10'							
	MPI Collectives	MPI_INIT						
	MPI Phase		1000	1003		1346		
	vierenase		1000	1003		1940		

Phase 1292 + 1293 + 1298 represent 99% of app's FOM Viewing Rank 192 MPI Phases for 8x32 Ranks of LAMMPS

Application Model: Kairos Selected Phases App Trace

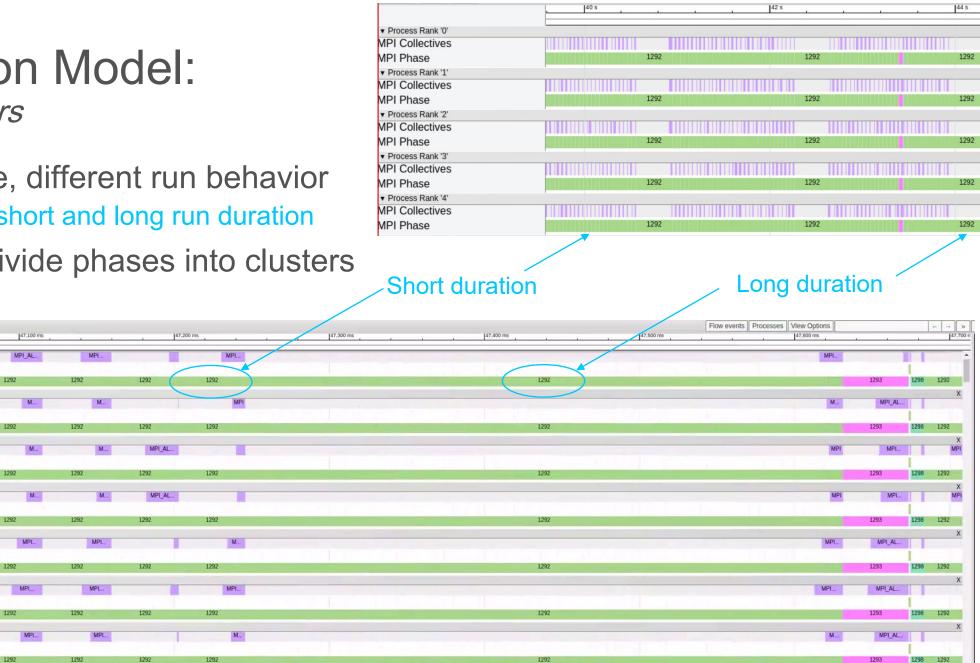
- Select phases that best represent FOM
 - Can filter out insignificant phases
 - Or can keep all phases in FOM





Application Model: Kairos Clusters

- Same phase, different run behavior
 - E.g. 1292 short and long run duration
- Behaviors divide phases into clusters



1292

1292

1292

1292

MPI_AL

MPI_AL

MPI_AL

Record Save Load selected apptrace.json

MPI Collectives Ignored Phases MPI Phase

· Process Rank '1'

MPI Collectives Ignored Phases MPI Phase

 Process Rank '2' MPI Collectives

Ignored Phases MPI Phase Process Rank '3' MPI Collectives

Ignored Phases MPI Phase

· Process Rank '4' MPI Collectives

Ignored Phases

Process Rank '5'

MPI Collectives

Ignored Phases **MPI** Phase

· Process Rank '6'

MPI Collectives Ignored Phases MPI Phase

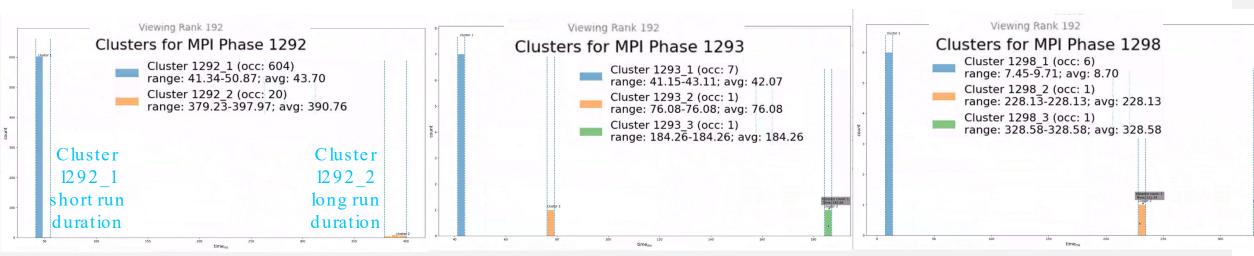
MPI Phase

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Application Model

- Represent app using clusters selected
- Application Model
 - $= \Sigma c luster x occurrence$

 $= (1292_1*604 + 1292_2*20) + (1293_1*7 + 1293_2*1 + 1293_3*1) + (1298_1*6 + 1298_2*1 + 1298_3*1)$ = (43.70 *604 + 390.76 *20) + (42.07 *7 + 76.08 *1 + 184.26 *1) + (8.70 *6 + 228.13 *1 + 328.58 *1) = 35,373.74 ms





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CPU, MPI Collectives, MPI APIs, Host-Driver calls, **Profiles**: Visualization of Cluster (Per Rank Behavior)

|50 ms 100 ms 150 ms |200 ms |250 ms |300 ms |350 ms 1400 ms Process Rank '0' MPI Collectives MPI_ALLREDUCE MPI Calls MPI M., MP MP M., M MPI... M. MPI... MPI MPI MPI SEND CPU LAM. LAM LAMMPS_NS::Modify::pos... LA LAMMPS_N LAMMPS NS::Neighbor::decide(I AM i LAM I AM I LAM LA LAM L... LA LAM I AM gb LAMMPS NS::FixGPU::pos... LA LAM LAM LAM LA L gb LA LAM LA Imp_gpu_forces(double**, dou.. Imp_gp.. Imp_gp.. LA Im HOST-Runtime 'einv009' cuEventSynchronize cuEventSvn. cuEven.. cuEventSvn.. cuEven. cuEventSvn. HW-Memory device 0 stream 14 4 HW-Kernel device 0 stream 14 MPI Phase Process Rank '1' MPI ALLREDUCE MPI_AL. MPI Collectives MPI Calls MPI SEND MPI MPI SEND M... MF MPI... M... M... M... CPU LAMMPS NS: Modify: pos LAM LA LA LAM LAMMPS NS::FixGPU::pos. Imp apu forces(double**, do Imp HOST-Runtime 'einv009' cuEventSynchronize cuEventSyn. cuEven... cuE... cuE... HW-Memory device 0 stream 14 1 + HW-Kernel device 0 stream 14 MPI Phase Process Rank '2' MPI ALLREDUCE MPI. MPI Collectives MPI Calls MPI. M... MPI_WAIT MPI_SEND MPI... M... MP M... CPU LAMMPS_NS::Modify::post_force(int) LA LA LAM LAMMP. LA LAMMP IΔM IAM I ΔM I AM LAMMPS_NS::FixGPU::post_force(int) LA LAN Imp_gpu_forces(double**, dou. Imp Imp... HOST-Runtime 'einv009' cuEventSynchroniz cuEventSyn cuE... cuEven 1 HW-Memory device 0 stream 14 HW-Kernel device 0 stream 14 MPI Phase Process Rank '3' MPI Collectives MPI_AL... MPI. MPI Calls MPI_WAIT M., M MPI_WAIT M... MPI WAIT CPU LAM. LAMMPS NS::Modify::post force(int) IAM IAM LA LAMMPS LA L LAM LAMMPS N i LA LA IA LA LAMMPS N LAM LAMMPS N I A LAMMPS NS:0 LAM a... LAMMPS NS::FixGPU::post force(int) LAM LAMMPS.. LAM Imp apu forces(double** dou Imp_gpu_f.. HOST-Runtime 'einv009' cuEventSynchronize cuEventSyn cuE... cuEventSvn. HW-Memory device 0 stream 14 HW-Kernel device 0 stream 14

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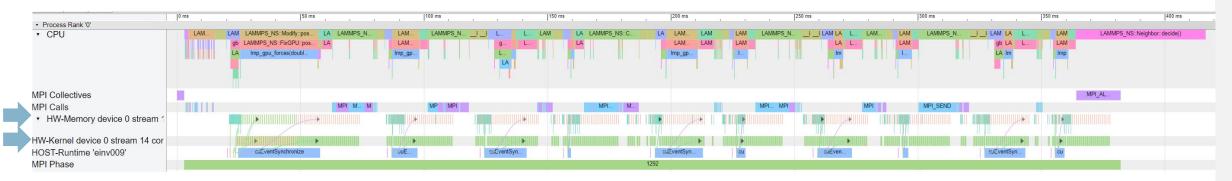
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Profile contains:

Mem transfers, Kernel calls, Kairos Phase

Profiles: *Visualization of Cluster (Per Device Behavior)*

Easy to see each GPU's device utilization/mem transfers with this view (32 ranks sharing one GPU)

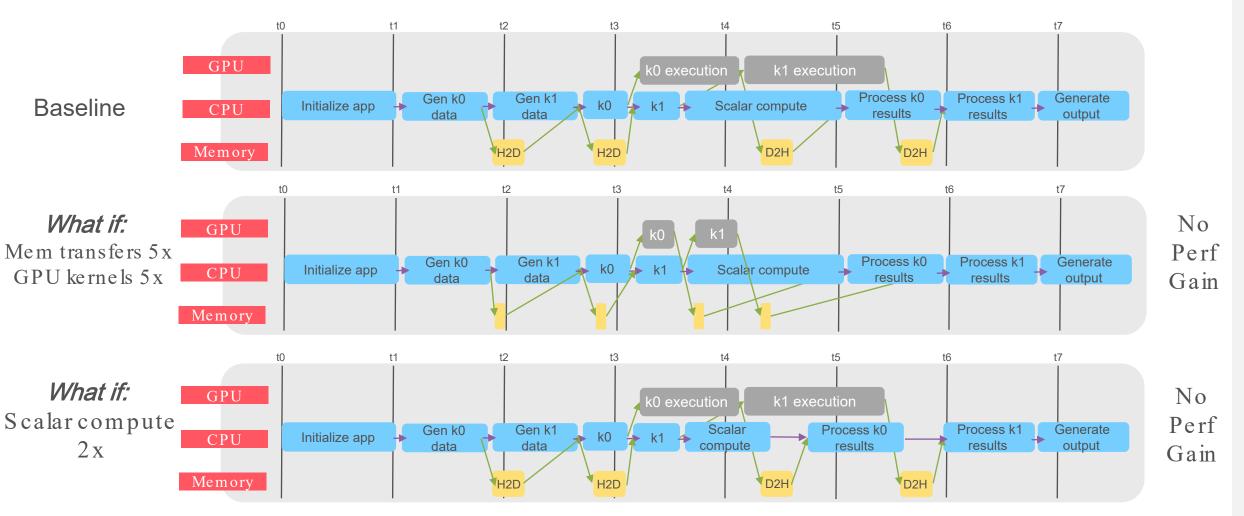


- View MPI Collectives, Calls, CPU data for single rank
- But view GPU sharing (all 32 rank's kernels and memory calls on same row)
- Create multi-level dependency analysis per device $\rightarrow DAG(s)$

Kairos Dependency Analysis / Critical Path:

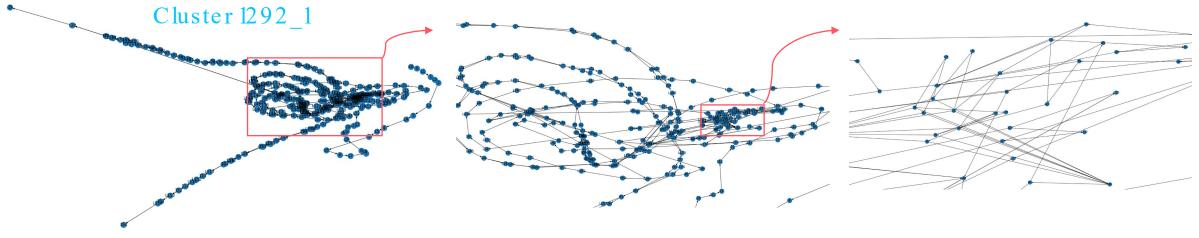
Example

Concurrent CPU, GPU, Memory Transfers



Kairos Dependency Analysis/Critical Path

- Previous simple example
 - Only single rank on CPU with single GPU
- On a real application, multiple levels of concurrency
 - Multiple nodes, multiple ranks, multiple GPUs, Communication, ...
- Kairos captures multi -level dependencies across execution regions creating multiple dependency graphs

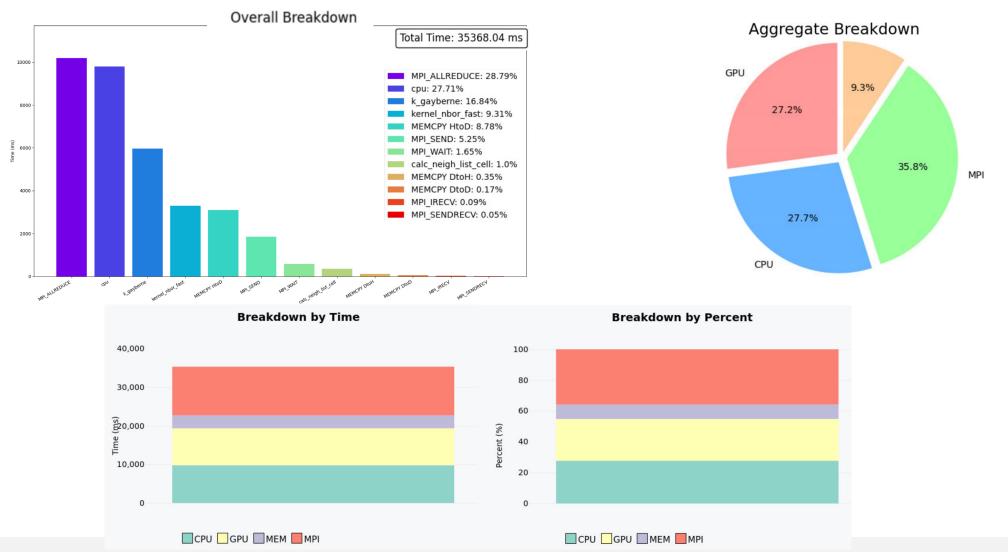


Actual Kairos dependency graphs, box locations approximate

Kairos Insights:

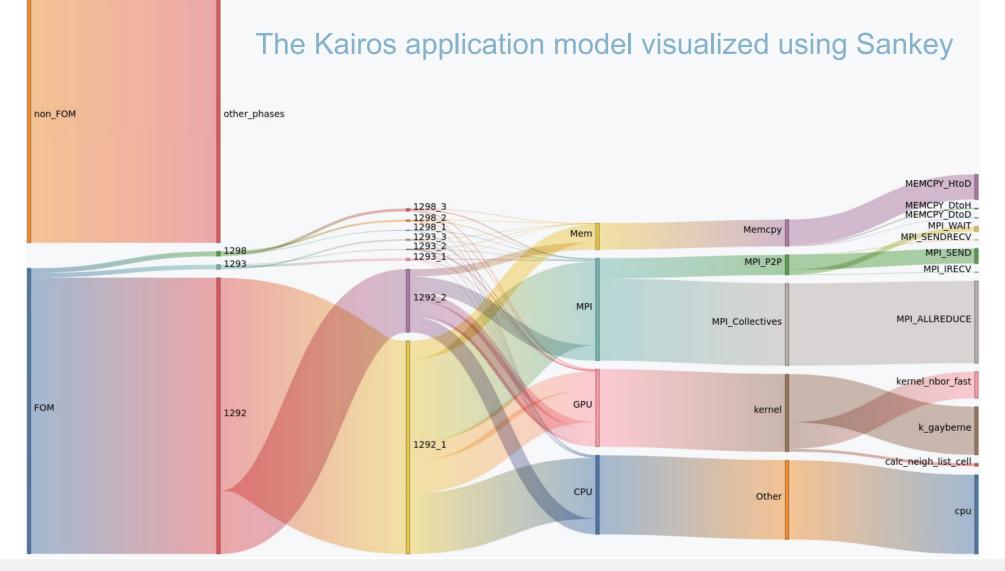
OverallBreakdown

Breakdown at cluster and app-level using critical path analysis from DAG



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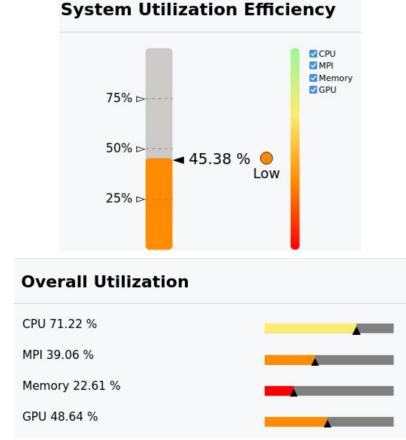
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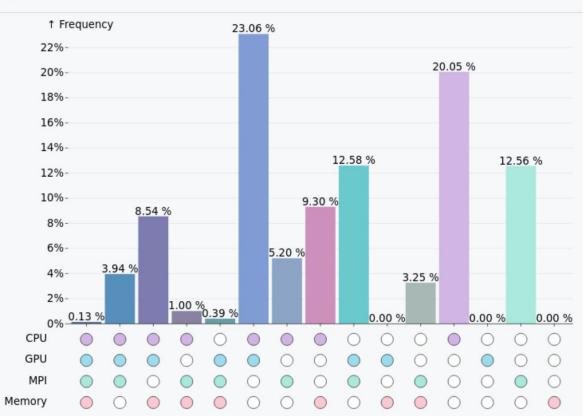
Kairos Insights

Concurrency at App-level

Useful when comparing across systems / projections



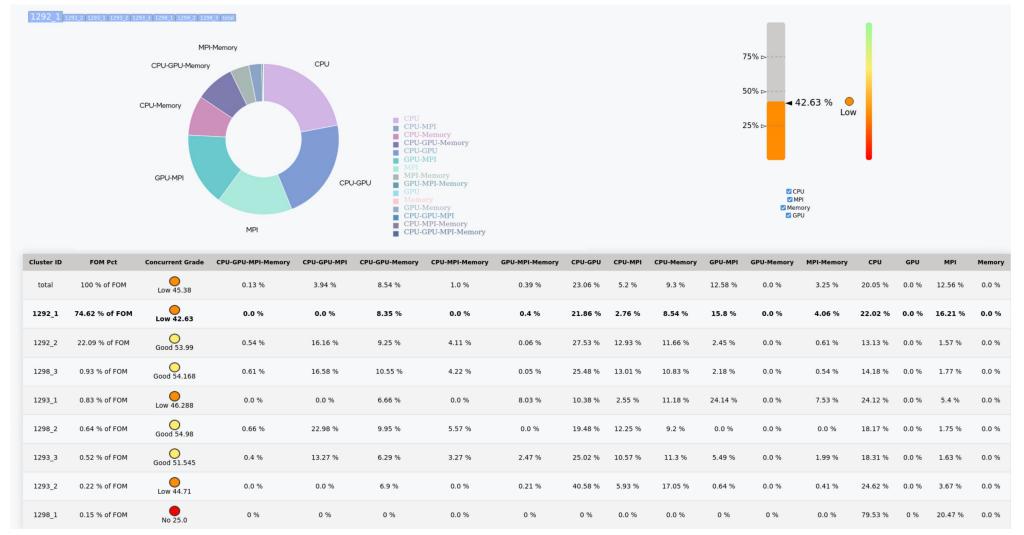
Overall Concurrency



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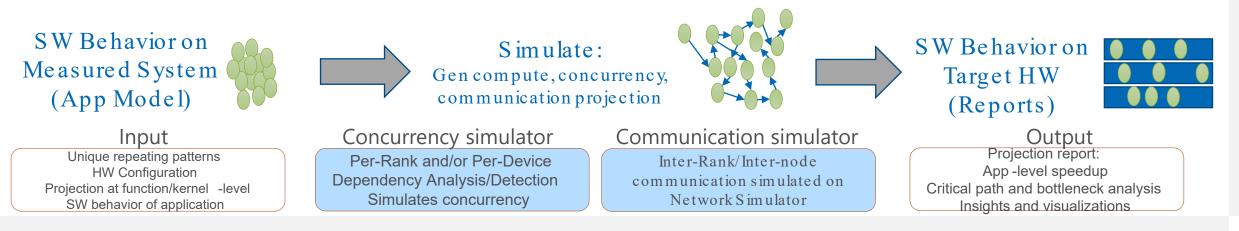
Example: Cluster 1292_1

Kairos Insights Concurrency at Clusterlevel



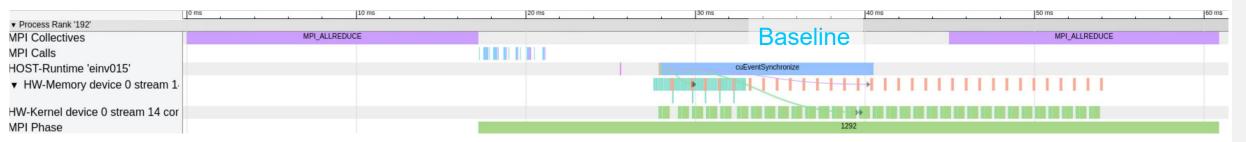
Projection/Pathfinding Capability

- The application model can be used to create projections and pathfinding experiments to influence HW system design
- At a high-level
 - Extracts app SW components and dependencies
 - New SW and HW (CPU/GPU/NW/system) configs for target system
 - Simulate on target system, generate compute, concurrency, communication projection
 - Experiment with potential system configurations for pathfinding

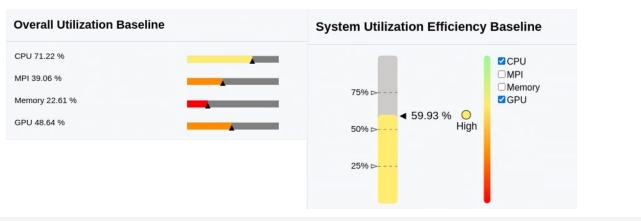


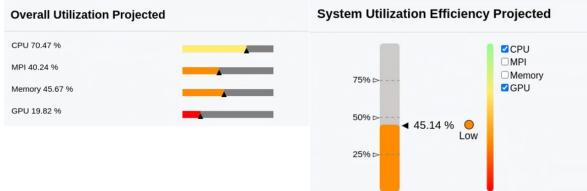


Kairos Projection



	10 ms	10 ms	20 ms	30 ms 40 ms	50 ms 60 ms
 Process Rank '192' 					
MPI Collectives	MPI_ALLREDUCE		MPI_ALLREDUCE	Projected	
MPI Calls				riojeeteu	
▼ HW-Memory device 0 stream 1.					
HW-Kernel device 0 stream 14 cor					
MPI Phase		1292			

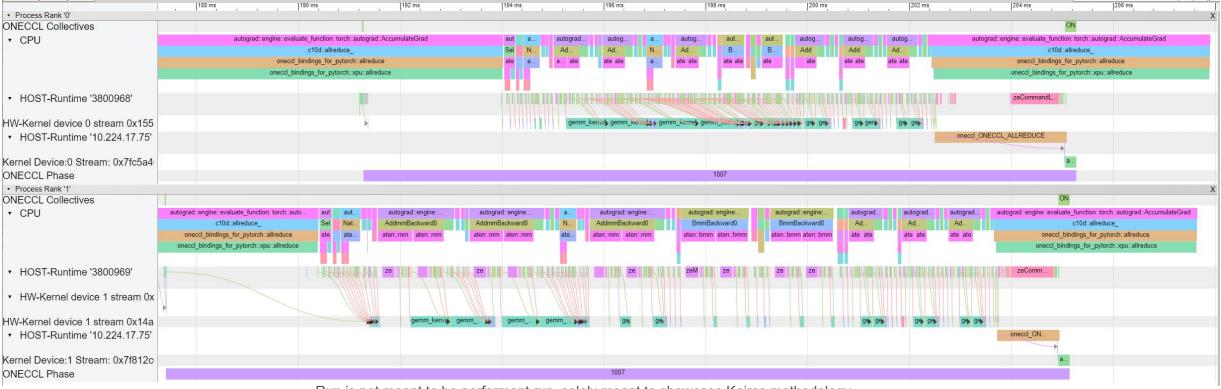




AI Example

Kairos supports AI applications

- AI Distributed Phases (OneCCL or NCCL based collectives)
- The same flow and insights apply including app -model and projections



Run is not meant to be performant run, solely meant to showcase Kairos methodology

Summary

Kairos is a unique capability not otherwise available inside or outside of Intel

- Methodology to analyze and project performance at system level
- Compares behaviors between
 - Multiple implementations of same application
 - Comparisons between different system configurations
- Enables projections of end -to-end system performance for future Intel/competitive platforms
 - Pathfinding experiments to evolve Intel architecture to better support HPC/AI
 - Quick, high confidence customer projections

Back-up



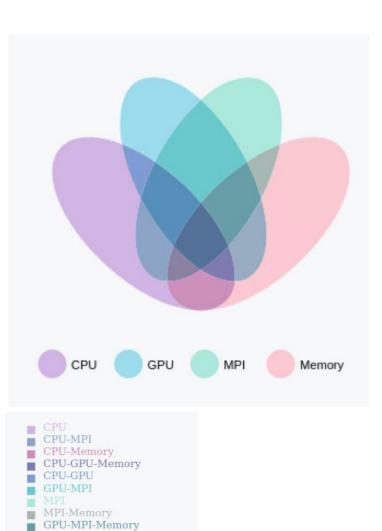
BERT Run and LAMMPS Config

- BERT:
 - app name: <u>text-classification</u> at huggingface /transformers
 - parameters: --model_name_or_path bert-base-cased --task_name mrpc --max_seq_length 128 --per_device_train_batch_size 32 -- learning_rate 2e-5
- Single Node SDP config for BERT run:
 - OS: Ubuntu 22.04.3 (kernel: 5.15.47+prerelease 23.4.120-1)
 - CPU: Intel(R) Xeon(R) Platinum 8360 Y CPU @ 2.40 GHz
 - GPU: Intel(R) Data Center GPU Max 1550
 - One API version: 2023.2.0
 - One CCL version: 2021.10.0
- LAMMPS:
 - IntelInternalversion
 - IntelInternalparameters
- Cluster configuration for LAMMPS run
 - OS: Rocky-Linux-8 (Linux version 4.18.0-477.21.1.e18_8.x86_64)
 - CPU: Intel(R) Xeon(R) Platinum 8360 Y CPU @ 2.40 GHz
 - 8 Nodes, 36 cores per socket, two sockets, per node
 - Memory: DDR4_16x16GB@3200MHz
 - GPU:2xNVIDIA A100-PCIE-40GB (but only 1GPU made visible per node during run)
 - Fabric: Mellanox_HDR
 - 32 ranks per node used with 2 OMP threads, 32 ranks per GPU

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Why Kairos?

- Multi-node AI and HPC applications utilize CPUs, GPUs and interconnect concurrently to maximize performance
- Questions
 - How do we simplify?
 - How do we understand concurrent behavior?
 - How can we extract meaningful insights (critical path, bottleneck analysis, multilevel dependency analysis)?
 - How can we use our understanding of app's behavior to project performance when SW/HW changes?
 - If optimizations improve current bottlenecks, what are the next-level performance limiters?
- Problems with traditional approaches
 - Data collection limited to single-node or small number of nodes
 - Data collection significantly impacting performance
 - Providing data too detailed for meaningful insights
 - Insufficient abstraction for overall understanding of application



CPU-GPU-MPI CPU-MPI-Memory CPU-GPU-MPI-Memory

Introducing Kairos

Methodology to analyze and project performance at system level

• Analysis Capabilities:

E2E application \rightarrow FOM*/Region of Interest \rightarrow App model \rightarrow Insights (multi-level dependency analysis, critical path, concurrency, ...)

• Projection Capabilities:

App Model+target configs (SW/HW) → Simulate on target platform
 (Compute projection, concurrency simulation, communication simulation) →
 Projected FOM → Insights

*FOM = Figure of Merit

