

IXPUG

Intel Xeon Phi Users Group

IXPUG Mission

Provide a forum for the *free exchange* of information that enhances the **usability** and **efficiency** of scientific and technical applications running on supercomputers based on the Intel Xeon Phi processor.

Intel Corporation is a sponsor of IXPUG, but IXPUG is not legally or organizationally affiliated with Intel.

Methods

- Connect application developers, Intel technical staff, and HPC center staff to share experiences and best practices and influence future products
 - Porting, Scaling, Algorithms
 - Using Intel software: compilers, libraries, tools
 - System installation, maintenance and resource management
 - Phi (manycore) processor architecture and technology
 - ...

IXPUG Activities

- User Meetings
 - Yearly in U.S. 2013, 2014 at TACC
 - July 2015 in Berkeley, CA
 - European/Asian meetings?
- SC BoFs
- Web site (ixpug.org)
 - Group activities calendar and archives
- Active community twiki
 - Tools
 - Compilers
 - Techniques
 - ...
- Training Events

**We're looking for
more ideas!**

History

- Originated at TACC
 - Two successful user meetings 2013, 2014
 - (<https://www.ixpug.org/>)
- Expanding to include other HPC centers
 - NERSC's announcement to field "Cori" with 9300+ KNL processors in 2016
 - Los Alamos/Sandia Trinity system
 - NERSC, TACC, Trinity + others working on a charter for expanding IXPUG

We welcome your input!

Highlights of Proposed Charter

- Independent organization (not part of Intel)
- Open discussions and info
- Site/institutional members
 - Phi-based HPC system sites
 - Institutional buy-in to support user group efforts
- Individual members
 - No additional restrictions, other than active participation
- Governing board made up of reps from site members, elected by all members
 - Plan and execute meetings and other activities
 - Run web site, mailing lists, etc.
- Activities open to anyone interested in using Phi for HPC

We welcome your input!

More on Proposed Charter

- Two meetings per year
 - One in U.S., one in Europe or Asia
 - High-quality presentations and discussions
- Meetings hosted at and by a site member
- No membership fees, if at all possible
- No meeting registration fees, if possible
- Support from Intel and other partners
 - Financial (food, refreshments, group activities, ...)
 - Commit staff to attend meetings, workshops, etc.; participate on twiki

We welcome your input!

July 2015 U.S. Meeting



NERSC's new building at
Berkeley Lab. Scheduled
to open in Summer 2015

July 20-21, 2015?

(ISC is July 12-16, 2015)



What Can You Do?

- Sign up for the IXPUG email list
- Participate in meetings and share your experience and knowledge
- Contribute to the twiki
- Volunteer to help plan and run activities
- Let us know what would be useful to you
 - Meetings?
 - Training?
 - Email lists?
 - Web forums?
 - Focused workshops?
 - Hack-a-thons?

Current Primary Contacts

- Richard Gerber – NERSC
 - ragerber@lbl.gov
- Kent Milfeld- TACC
 - milfeld@tacc.utexas.edu
- We will set up an IXPUG contact email address/list.