

# Intel Xeon Phi Users Group (IXPUG) Charter

## Summary

IXPUG is an independent users group whose mission is to provide a forum for the free exchange of information that enhances the usability and efficiency of scientific and technical applications running on large High Performance Computing (HPC) systems (supercomputers) using the Intel Xeon Phi processor. IXPUG is administered by representatives of member sites that operate large Phi-based HPC systems. Participation in IXPUG activities is open to anyone interested in using the Phi for large-scale scientific or technical computing. IXPUG holds meetings and other activities as determined by its members to further its mission. Participants include staff from member sites, users of member sites' facilities, Intel staff, and others with an interest in using the Intel Xeon Phi for scientific computing on large HPC systems.

Intel Corporation helps sponsor IXPUG, and may appoint an individual to serve on the Steering Committee.

## Mission

The mission of the Intel Xeon Phi Scientific Computing Users Group (IXPUG) is to provide a forum for the free exchange of information that enhances the usability and efficiency of scientific and technical applications running on large High Performance Computing (HPC) systems (supercomputers) using the Intel Xeon Phi processor.

## Organization

IXPUG activities are run by a Leadership Board made up of the following officials, all elected by a **majority vote of the Steering Committee members**: (1) a President, (2) a Vice-President, and (3) a Secretary.

These officers will function in the following roles and have the prescribed authority in the organization.

- President: Presides over meetings and their organization. Insures that meeting agendas and operations adhere to charter policies and protocol.
- Vice President: Performs the functions of the president when president is not in attendance at meetings, or when specific duties are delegated to the VP.
- Secretary: Oversees management of the web site and assures that documents are archived in a location accessible to members and the public. The secretary is also responsible for IXPUG correspondence and reports.

Officers will be elected to serve two-year terms. If the President is unable or does not fulfill the presidential duties, or resigns, the Vice-President will assume the presidential duties and complete the President's term. A vacancy of the Vice-President or Secretary will be filled by an email election of IXPUG organizational members. **IXPUG Officers do not have to be employed by one of the Steering Committee organizations.**

Vendor members may not serve as officers.

## Membership and Participation

Membership is open to researchers who are using the Xeon Phi, and organizations that run large HPC systems that contain Intel Xeon Phi processors or are in the process of acquiring such systems.

### Steering Committee

A Steering Committee will be formed of representatives of the charter members of IXPUG, including: the Texas Advanced Computing Center (TACC), the National Energy Research Scientific Computing Center (NERSC), the Argonne Leadership Computing Facility (ALCC), Lawrence Berkeley National Labs (LBNL), the University of Tennessee at Knoxville, NASA Goddard, the Louisiana State University (LSU), Purdue University, **Germany-TBD, Japan-TBD, and CINECA (Italy).**

Steering committee member representatives must attend a minimum of one meeting per year; non-attendance may result in removal of that organization from the steering committee.

Vendors may apply for membership in the Steering Committee; however, vendor participation will be limited to 3 members.

### Membership Application

A center may apply for Steering Committee membership through the Web Application Form.

- a) Site/Institution Name
- b) Location
- c) URL
- d) Proposed IXPUG center representative(s): name, email address, telephone
- e) Details on each of the following:
  - a. How many Xeon Phis/KNLs do you have?
  - b. What applications are you running?
  - c. Are you offering training to staff/students at your institution?
  - d. *[any other questions we should ask?]***
- f) Who referred you to IXPUG?

Membership acceptance will be evaluated on the areas listed in item e. A simple majority vote of steering committee members via email is required for acceptance. Membership applications will be archived for future reference.

Any interested individual may sign up for the IXPUG mailing list and participate in discussions on the IXPUG website. The mailing list will be moderated to ensure a minimum of disruption. A separate mailing list will be maintained for the Steering Committee.

### Membership Fees

There are no fees associated with IXPUG membership. The goal of IXPUG is to fund activities through corporate sponsorship without the need to collect membership or registration fees.

Sponsorship by Intel and other interested parties is encouraged. The Leadership Board may consider the possibility of imposing fees if it becomes necessary.

## **Meetings**

IXPUG will hold two general meetings per year; One hosted at a member site, and the other around the Supercomputing XY conference in November. Additional meetings may be held at ISC or other convenient venues.

The meetings are for the purpose of (1) sharing experiences, best practices, and other information that furthers the purpose of the organization among members and their users, and (2) engaging with Intel on issues related to efficiently and effectively using Phi processors for HPC.

Meeting hosts are expected to provide meeting space at little to no cost, to avoid registration fees for participants. Member sites and the Intel team are expected to participate actively in the meetings and to cover their own costs for participation. All meetings and the information shared at the meetings shall be open and freely accessible to the HPC community. Presentations, minutes, proceedings and other information shall be available on the IXPUG web site.

## **Web Site**

IXPUG will maintain a web site ([www.ixpug.org](http://www.ixpug.org)) that contains information about IXPUG and its activities, allows meeting registration, and archives presentations and other documents and media related to IXPUG activities.

## **NDA and Proprietary Information**

All information associated with IXPUG meetings, events, and online discussions are considered open and freely distributable. If parties wish to discuss NDA or proprietary information in association with an IXPUG activity, it should be made clear to all parties that these discussions are not affiliated with IXPUG.