

Intel Xeon Phi Users Group (IXPUG) Charter

Summary

IXPUG is an independent users group whose mission is to provide a forum for the free exchange of information that enhances the usability and efficiency of scientific and technical applications running on large High Performance Computing (HPC) systems (supercomputers) using the Intel Xeon Phi processor. IXPUG is administered by representatives of member sites that operate large Phi-based HPC systems. Participation in IXPUG activities is open to anyone interested in using the Phi for large-scale scientific or technical computing. IXPUG holds meetings and other activities as determined by its members to further its mission. Participants include staff from member sites, users of member sites' facilities, Intel staff, and others with an interest in using the Intel Xeon Phi for scientific computing on large HPC systems.

Intel Corporation is an Official Sponsor (see **Official Sponsors** below) of IXPUG, and may appoint an individual to serve on the Steering Committee.

Mission

The mission of the Intel Xeon Phi Users Group (IXPUG) is to provide a forum for the free exchange of information that enhances the usability and efficiency of scientific and technical applications running on large High Performance Computing (HPC) systems (supercomputers) using the Intel Xeon Phi processor.

Organization

IXPUG is organized into the following groups, each with the role specified in the table below.

Group	Role	Eligibility
Leadership Board	Conducts IXPUG activities	Members
Steering Committee	Elects and advises leadership board; manages Organizational Membership.	Appointed representatives from Organizational Members.
Organizational Members	Appoint representatives to the Steering Committee	Organizations that are actively and significantly engaged in using the Intel Xeon Phi in scientific or engineering High Performance Computing.

Members	Actively participate in events and activities.	Any individual with an interest in using the Intel Xeon Phi in scientific or engineering High Performance Computing. Must have attended at least one IXPUG activity in the previous 18 months.
Official Sponsors	Actively participate in events and activities.	Any organization that fulfills the requirements of an Official Sponsor.

Leadership Board

IXPUG activities are run by a Leadership Board made up of the following officers, elected by a majority vote of the Steering Committee members: (1) a President, (2) a Vice-President, and (3) a Secretary.

These officers will function in the following roles and have the prescribed authority in the organization.

- **President:** Coordinates IXPUG meetings and activities. Leads and represents IXPUG at meetings and in the community. Insures that IXPUG activities adhere to charter policies and protocol.
- **Vice President:** Performs the functions of the president when president is not in attendance at meetings, or when specific duties are delegated to the VP.
- **Secretary:** Manages IXPUG documents and manages the web site. Assures that documents are archived in a location accessible to members and the public. The secretary is also responsible for IXPUG correspondence and reports as well as managing elections.

Officers will be elected to serve two-year terms, which may be extended to three years by a majority vote of the Steering Committee. If the President is unable or does not fulfill the presidential duties, or resigns, the Vice-President will assume the presidential duties and complete the President's term. A vacancy of the Vice-President or Secretary will be filled by a majority vote of the Steering Committee.

- Officers may be removed by a two-thirds vote of the Steering Committee.
- IXPUG Officers do not have to be employed by one of the Steering Committee organizations.
- Representatives of Official Sponsors may not serve as officers.

Membership and Participation in IXPUG Activities

Membership is open to individuals and organizations interested in using the Intel Xeon Phi for scientific research or technical engineering. Participation in IXPUG activities is open to anyone interested in using the Intel Xeon Phi for HPC.

Steering Committee

A Steering Committee initially will be formed of representatives of the charter members of IXPUG, including: the Texas Advanced Computing Center (TACC), the National Energy Research Scientific Computing Center (NERSC) at Lawrence Berkeley National Laboratory, the Argonne Leadership Computing Facility (ALCC) at Argonne National Laboratory, Zuse Institute Berlin (ZiB), Rouge Wave Software, Juelich Supercomputing Center, and Intel Corporation.

Steering committee member representatives must attend a minimum of one meeting per year; non-attendance may result in removal of that organization from the steering committee (by a two-thirds vote of the Steering Committee).

The Steering Committee considers applications for Organizational Members and may approve new Organization Members by a majority vote.

The Steering Committee may remove an Organizational Member by a two-thirds vote.

Official Sponsors may apply for membership in the Steering Committee; however, they may make up no more than 20 percent of the Steering Committee membership.

All votes by the Steering Committee require a quorum of at least 80 percent of Steering Committee members. Any Steering Committee member who does not vote in two consecutive elections will be removed from the Steering Committee.

Organizational Members

Non-commercial organizations that are actively and significantly engaged in using the Intel Xeon Phi for scientific or engineering High Performance Computing may apply to be Organizational Members of IXPUG. If approved by the Steering Committee, an Organizational Member may appoint one individual to serve on the Steering Committee.

Organizational Members may advertise themselves as being official members of IXPUG and will be listed as such on the IXPUG web site and, optionally at IXPUG's discretion, on other group documents.

Members

Any individuals with an interest in using the Intel Xeon Phi in scientific or engineering HPC, and who has attended at least one IXPUG activity in the previous 18 months, is considered a member of IXPUG.

Official Sponsors

Commercial organizations with a significant interest in supporting scientific and technical HPC on the Intel Xeon Phi may apply to be Official Sponsors of IXPUG. Official Sponsors must make a commitment to agree to donate a minimum of U.S. \$3,000 per year to support IXPUG activities. Official Sponsors will be listed as such on the IXPUG web site, and on IXPUG documents related to meetings and other sponsored activities.

IXPUG is not incorporated as an official organization, and therefore does not have the ability to conduct monetary transactions. Donations from sponsors will be paid directly to the providers of services involved with conducting meetings and other activities.

Official Sponsors will be approved by a majority vote of the Steering Committee.

Application for Organization Membership or Official Sponsorship

An organization may apply an Organizational Member or Official Sponsor by sending to the following information to membership@ixpug.org, or may apply online via the IXPUG.org website.

- a) Organization Name
- b) Membership Type (Organizational or Official Sponsor)
- c) Location
- d) Web Site
- e) Proposed IXPUG representative(s): name, email address, telephone
- f) Describe the Intel Xeon Phi-based systems hosted by your organization.
- g) What fields of science or engineering do you support?
- h) What applications do you support?
- i) Do you conduct training/education for your users? Please describe.
- j) Who referred you to IXPUG?

Membership acceptance will be evaluated based on the responses to the items above. A simple majority vote of steering committee members is required for acceptance. Membership applications will be archived for future reference.

Mailing List

Any interested individual may sign up for the IXPUG mailing list and participate in discussions on the IXPUG website. The mailing list will be moderated to ensure a minimum of disruption. A separate mailing list will be maintained for the Steering Committee.

Membership Fees

There are no fees associated with IXPUG membership. The goal of IXPUG is to fund activities through corporate sponsorship without the need to collect membership or registration fees. The Leadership Board may consider the possibility of imposing fees if it becomes necessary. Changes in the fee structure must be approved by a majority vote of the Steering Committee.

Meetings

IXPUG will hold two general meetings per year hosted at Organizational Member sites, in addition to gathering at other venues where appropriate.

The meetings are for the purpose of (1) sharing experiences, best practices, and other information that furthers the purpose of the organization among members and their users, and (2) engaging with Intel on issues related to efficiently and effectively using Phi processors for HPC.

Meeting hosts are expected to provide meeting space at little to no cost in order to avoid registration fees for participants. Organizational Members and Official Sponsors are expected to participate actively in the meetings and to cover their own costs for participation. All meetings and the information shared at the meetings shall be open and freely accessible to the HPC community. Presentations, minutes, proceedings and other information shall be available on the IXPUG web site.

IXPUG does not provide financial assistance to attendees, nor speaker fees at meetings or other events.

Web Site

IXPUG will maintain a web site (www.ixpug.org) that contains information about IXPUG and its activities, allows meeting registration, and archives presentations and other documents and media related to IXPUG activities.

NDA and Proprietary Information

All information associated with IXPUG meetings, events, and online discussions are considered open and freely distributable. If parties wish to discuss NDA or proprietary information in association with an IXPUG activity, it should be made clear to all parties that these discussions are not affiliated with IXPUG.

Charter

The charter may be modified by a majority vote of the Steering Committee.